

■ Features

- High Output Power: P1dB=39.0 dBm(Typ.)
- High Gain: G1dB=10.0dB (Typ.)
- High Power Added Efficiency : PAE=34% (Typ.)
- Broad Band: Frequency=7.1 to 7.9GHz
- Internally Matched
- Plastic Package for SMT applications

■ Description

The ELM7179-7PS is a power GaAs FET that is internally matched for standard communication bands to provide optimum power and gain.



ABSOLUTE MAXIMUM RATING (Case Temperature T_c=25 deg.C)

Item	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	15	V
Gate-Source Voltage	V _{GS}	-5	V
Total Power Dissipation	P _T	50	W
Storage Temperature	T _{stg}	-40 to +125	deg.C
Channel Temperature	T _{ch}	175	deg.C

RECOMMENDED OPERATING CONDITION

Item	Symbol	Condition	Limit	Unit
Drain-Source Voltage	V _{DS}		<10	V
Forward Gate Current	I _{GF}	Rg=100ohm	<+16	mA
Reverse Gate Current	I _{GR}	Rg=100ohm	> -2.2	mA
Channel Temperature	T _{ch}		155	deg.C

ELECTRICAL CHARACTERISTICS (Case Temperature T_c=25 deg.C)

Item	Symbol	Condition	Limit			Unit	
			Min.	Typ.	Max.		
Saturated Drain Current	I _{DSS}	VDS=5V, VGS=0V	-	3400	5200	mA	
Trans Conductance	g _m	VDS=5V, IDS=2200mA	-	3400	-	mS	
Pinch-off Voltage	V _p	VDS=5V, IDS=170mA	-0.5	-1.5	-3.0	V	
Gate-Source Breakdown Voltage	V _{GSO}	IGS=-170uA	-5.0	-	-	V	
Output Power at 1dB G.C.P.	P _{1dB}	VDS=10V Ids(DC)=2200mA(typ.) f=7.1 to 7.9 GHz	38.0	39.0	-	dBm	
Power Gain at 1dB G.C.P.	G _{1dB}		8.5	10.0	-	dB	
Drain Current	I _{DSR}		-	2200	2600	mA	
Power Added Efficiency	PAE		-	34.0	-	%	
Gain Flatness	ΔG		-	-	1.2	dB	
3rd Order Inter Modulation Distortion	IM ₃		f=7.9GHz Δf=10MHz, 2-tone Test Pout=28.0dBm (S.C.L.)	-40.0	-43.0	-	dBc
Thermal Resistance	R _{th}		Channel to Case	-	2.5	3.0	deg.C/W
Channel Temperature Rise	ΔT _{ch}	(V _{DS} × I _{DSR} - Pout + Pin) × R _{th}	-	-	80.0	deg.C	

G.C.P. : Gain Compression Point, S.C.L. : Single Carrier Level

CASE STYLE	I2C	
RoHS Compliance	YES	
ESD	Class 3A	4000V to < 8000V
MSL	2	One year after opening the packing

Note : Based on ANSI/ESDA/JEDEC JS-001-2012(C=100pF, R=1.5kohm)

Ordering Information

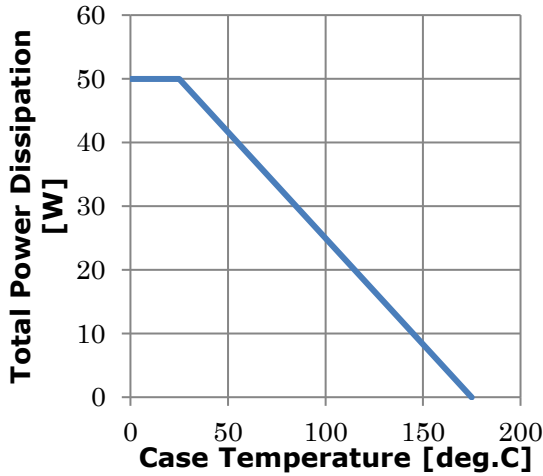
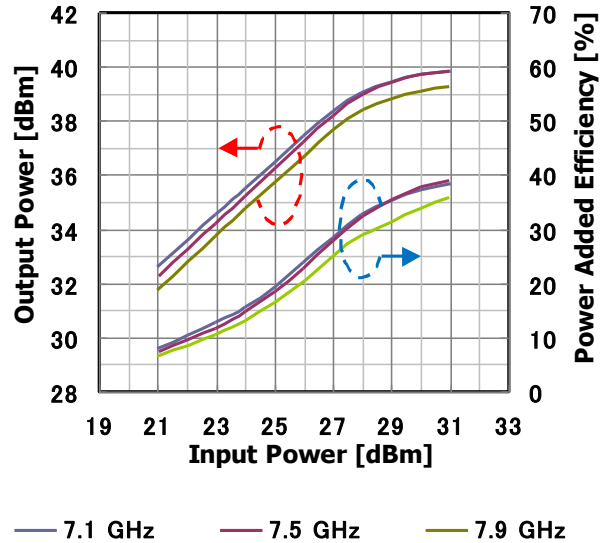
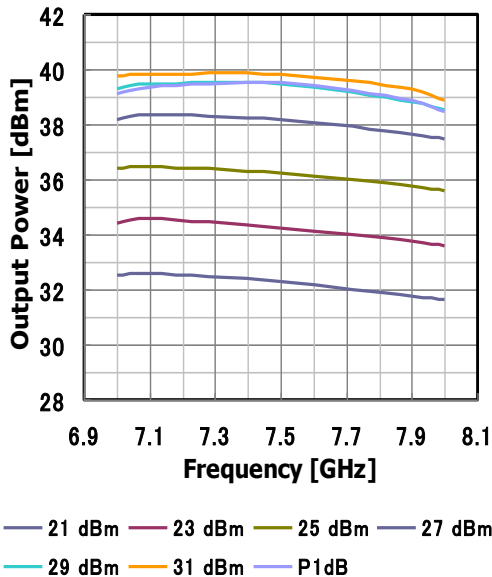
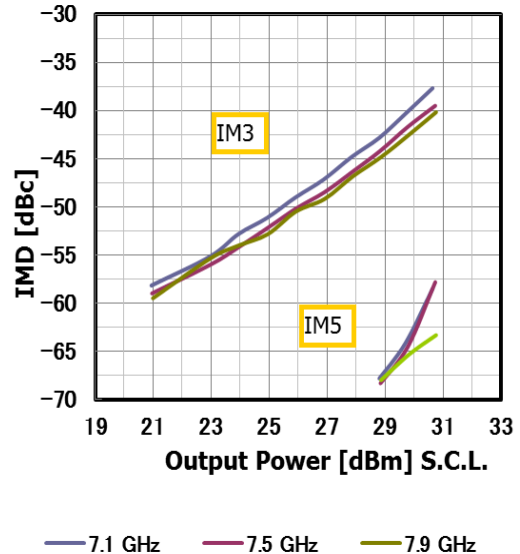
Model Type	MOQ	MOU	Packing Style
ELM7179-7PS	15pcs	No Limitation	50pcs-max./Tray , 1Tray-max./Packing
ELM7179-7PST	500pcs	500pcs	24mm width Tape (500pcs/Reel)

* MOQ stands for Minimum Order Quantity.

* MOU stands for Minimum Order Unit size.

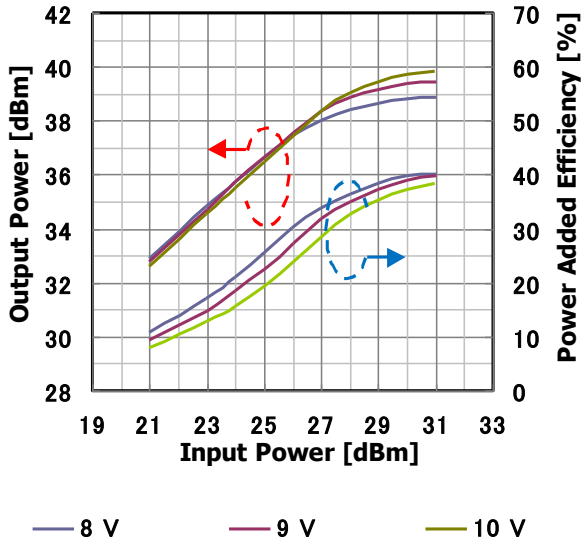
Note

- This device will not be delivered with test data but tested pass/fail 100% against DC and RF specifications.
- NO liquid cleaning process is suitable for this device. (including de-ionized water or solvent)

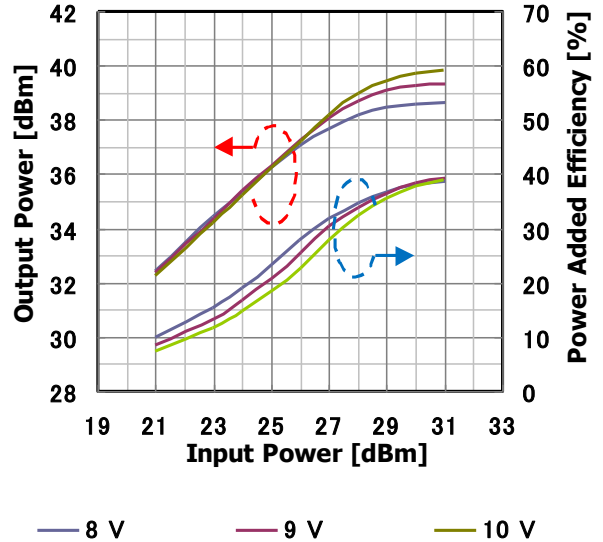
● RF Characteristics
Power Derating Curve

Input Power vs. Output Power and Power Added Efficiency
 $V_{DS}=10V, I_{DS(DC)}=2200mA$

Output Power vs. Frequency
 $V_{DS}=10V, I_{DS(DC)}=2200mA$

IMD vs. Output Power
 $V_{DS}=10V, I_{DS(DC)}=2200mA$


● RF Characteristics

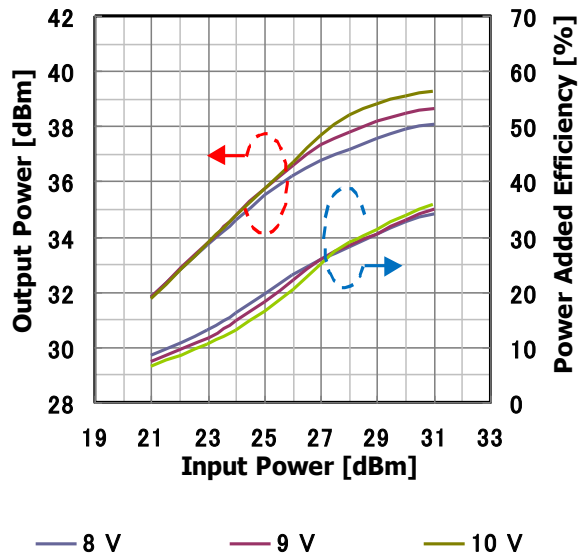
Input Power vs. Output Power, Power Added Efficiency by Drain Voltage
IDS(DC)=2200mA @7.1GHz

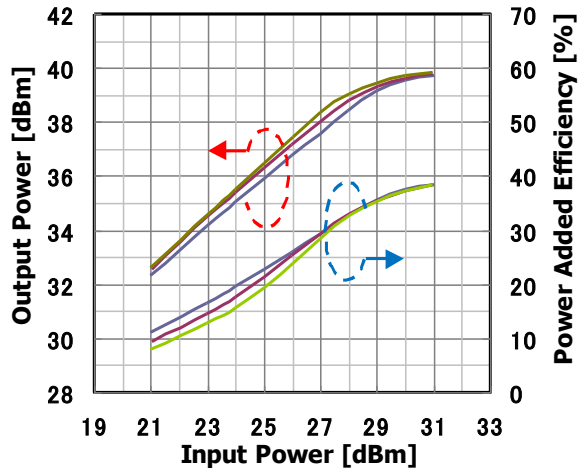


Input Power vs. Output Power, Power Added Efficiency by Drain Voltage
IDS(DC)=2200mA @7.5GHz

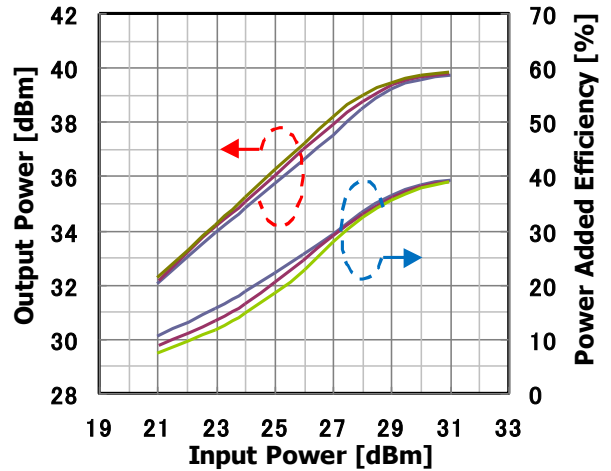


Input Power vs. Output Power, Power Added Efficiency by Drain Voltage
IDS(DC)=2200mA @7.9GHz

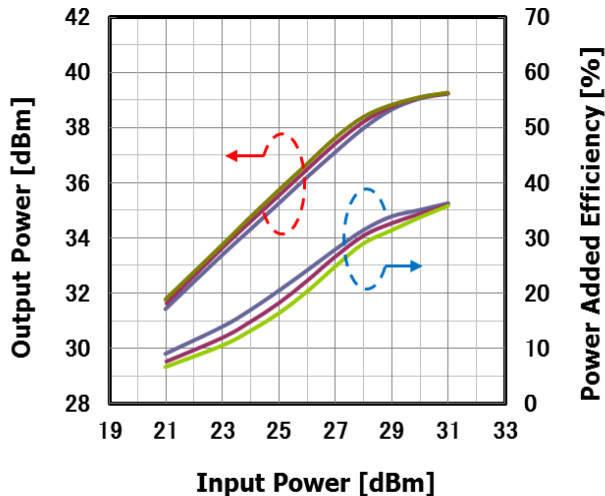


● RF Characteristics
**Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current
VDS=10V @7.1GHz**


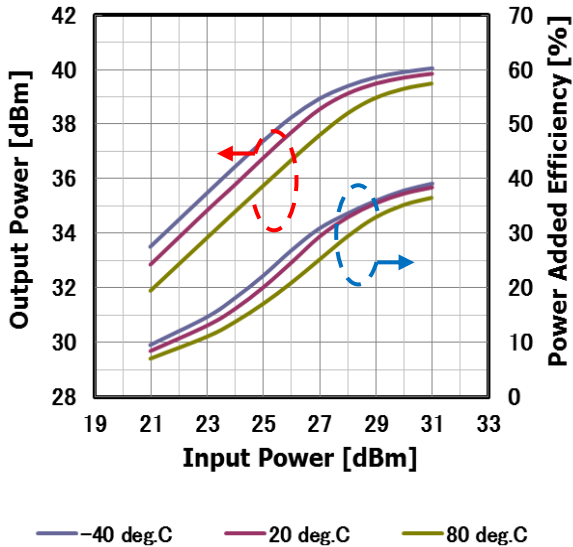
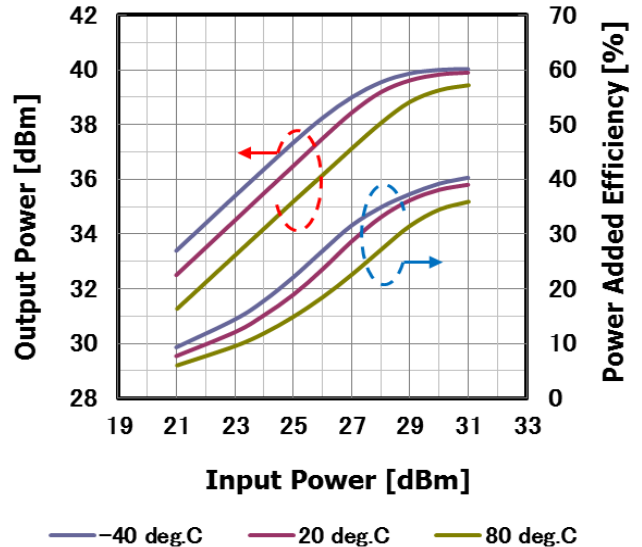
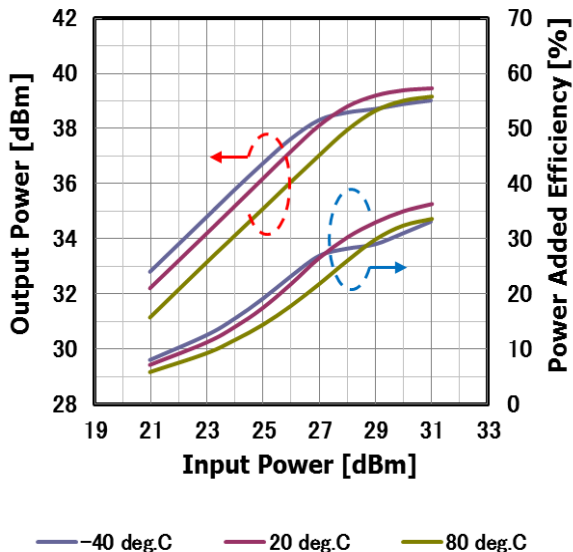
— 1400 mA — 1800 mA — 2200 mA

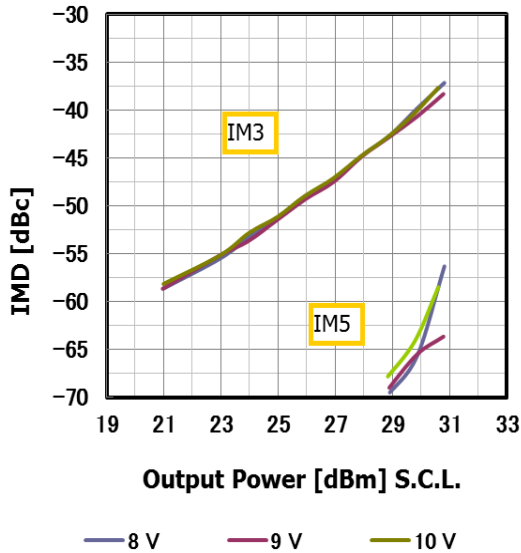
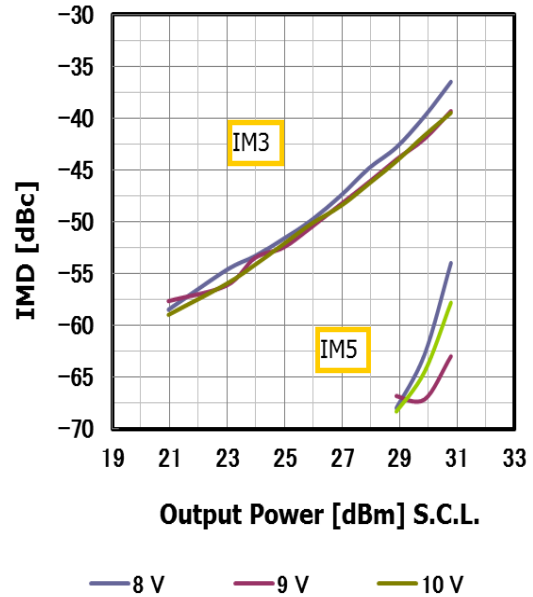
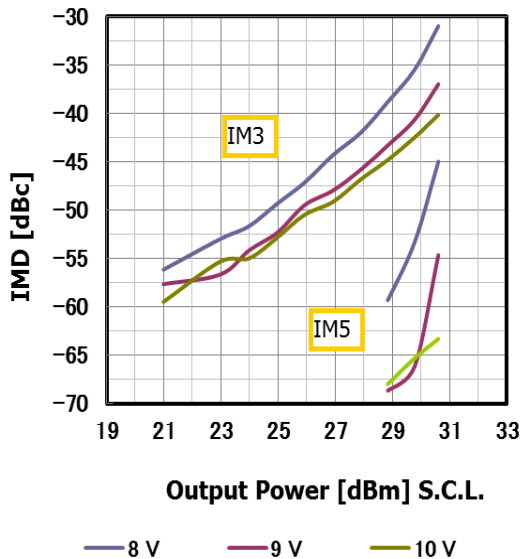
**Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current
VDS=10V @7.5GHz**


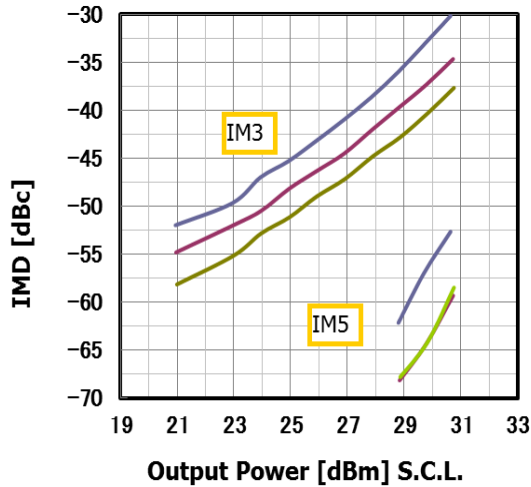
— 1400 mA — 1800 mA — 2200 mA

**Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current
VDS=10V @7.9GHz**


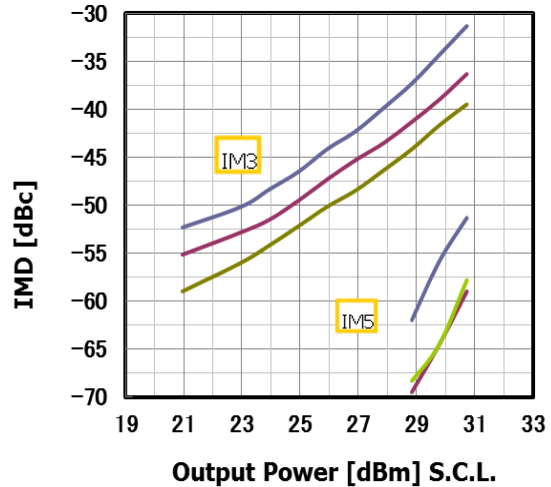
— 1400 mA — 1800 mA — 2200 mA

● RF Characteristics
Input Power vs. Output Power, Power Added Efficiency by Case Temperature
VDS=10V IDS(DC)=2200mA @7.1GHz

Input Power vs. Output Power, Power Added Efficiency by Case Temperature
VDS=10V IDS(DC)=2200mA @7.5GHz

Input Power vs. Output Power, Power Added Efficiency by Case Temperature
VDS=10V IDS(DC)=2200mA @7.9GHz


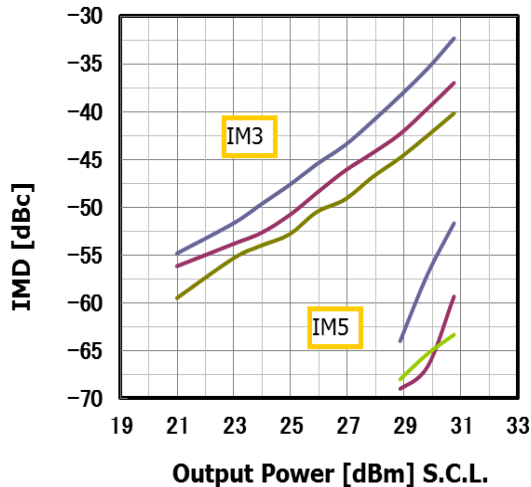
● RF Characteristics
**IMD Performance vs. Output Power
by Drain Voltage**
IDS(DC)=2200mA @7.1GHz

**IMD Performance vs. Output Power
by Drain Voltage**
IDS(DC)=2200mA @7.5GHz

**IMD Performance vs. Output Power
by Drain Voltage**
IDS(DC)=2200mA @7.9GHz


● RF Characteristics
IMD Performance vs. Output Power by Quiescent Drain Current
VDS=10V @7.1GHz


— 1400 mA — 1800 mA — 2200 mA

IMD Performance vs. Output Power by Quiescent Drain Current
VDS=10V @7.5GHz


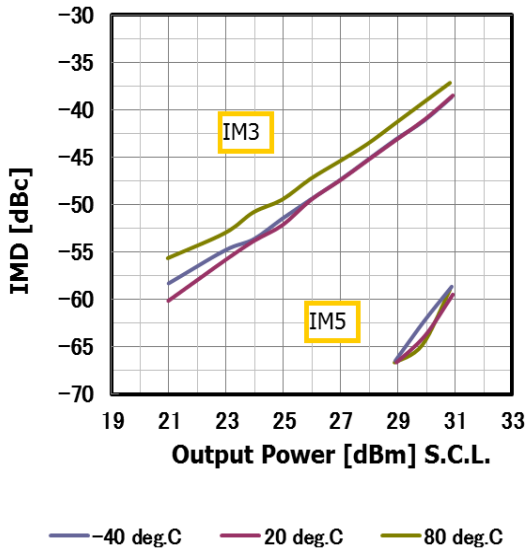
— 1400 mA — 1800 mA — 2200 mA

IMD Performance vs. Output Power by Quiescent Drain Current
VDS=10V @7.9GHz


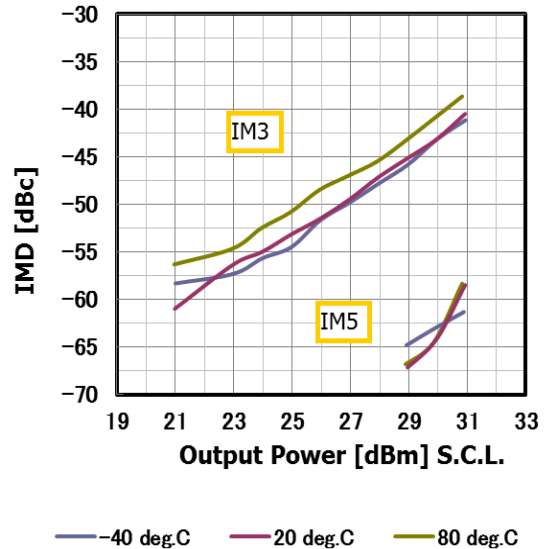
— 1400 mA — 1800 mA — 2200 mA

● RF Characteristics

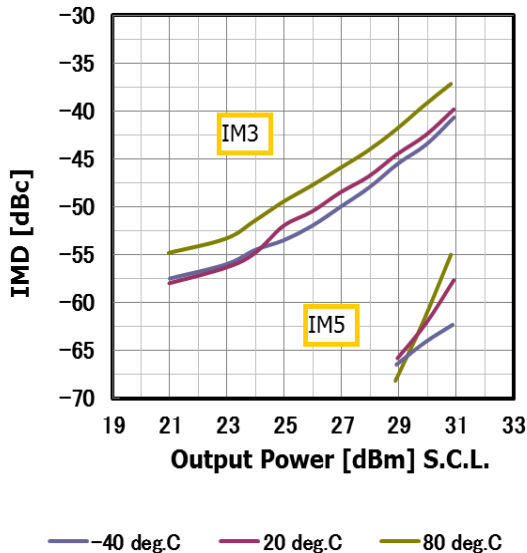
**IMD Performance vs. Output Power
by Case Temperature**
VDS=10V IDS(DC)=2200mA @7.1GHz

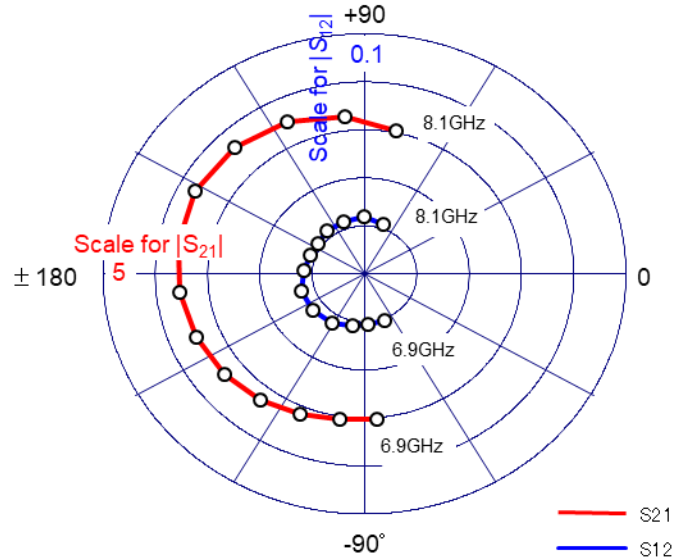
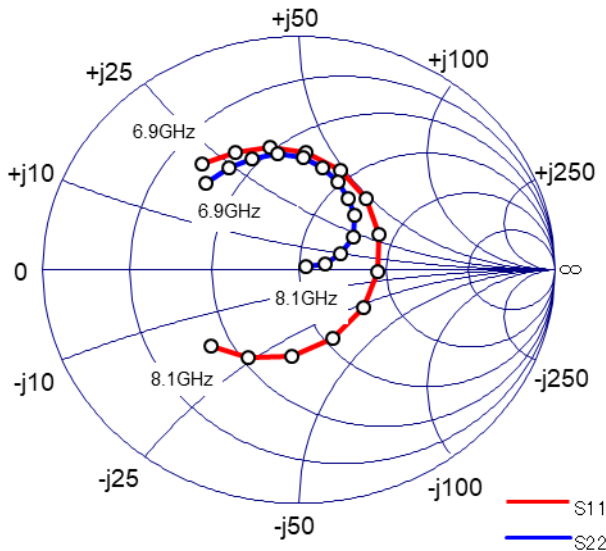


**IMD Performance vs. Output Power
by Case Temperature**
VDS=10V IDS(DC)=2200mA @7.5GHz

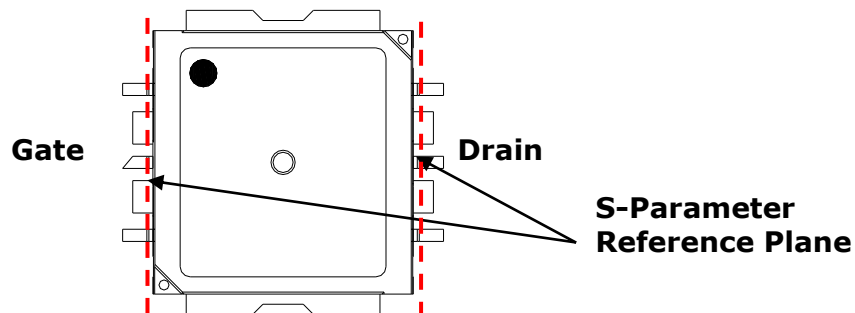


**IMD Performance vs. Output Power
by Case Temperature**
VDS=10V IDS(DC)=2200mA @7.9GHz

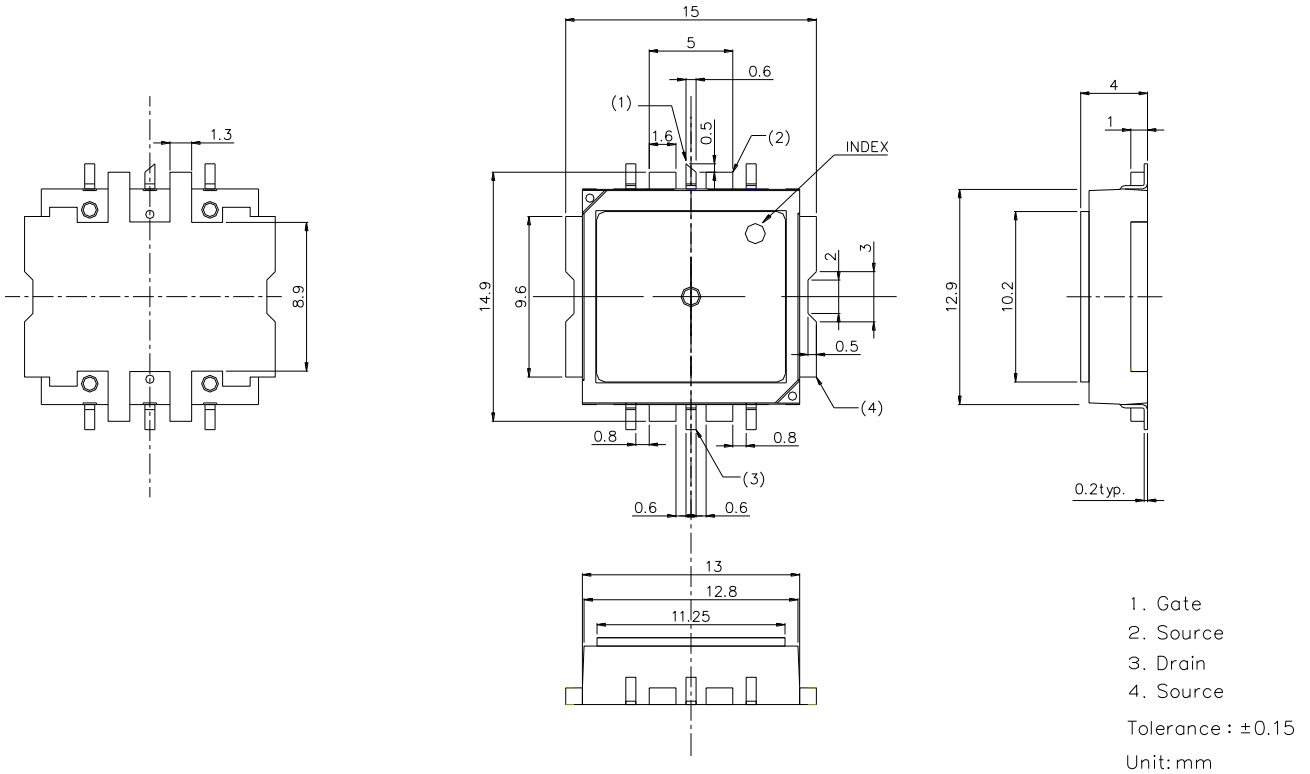


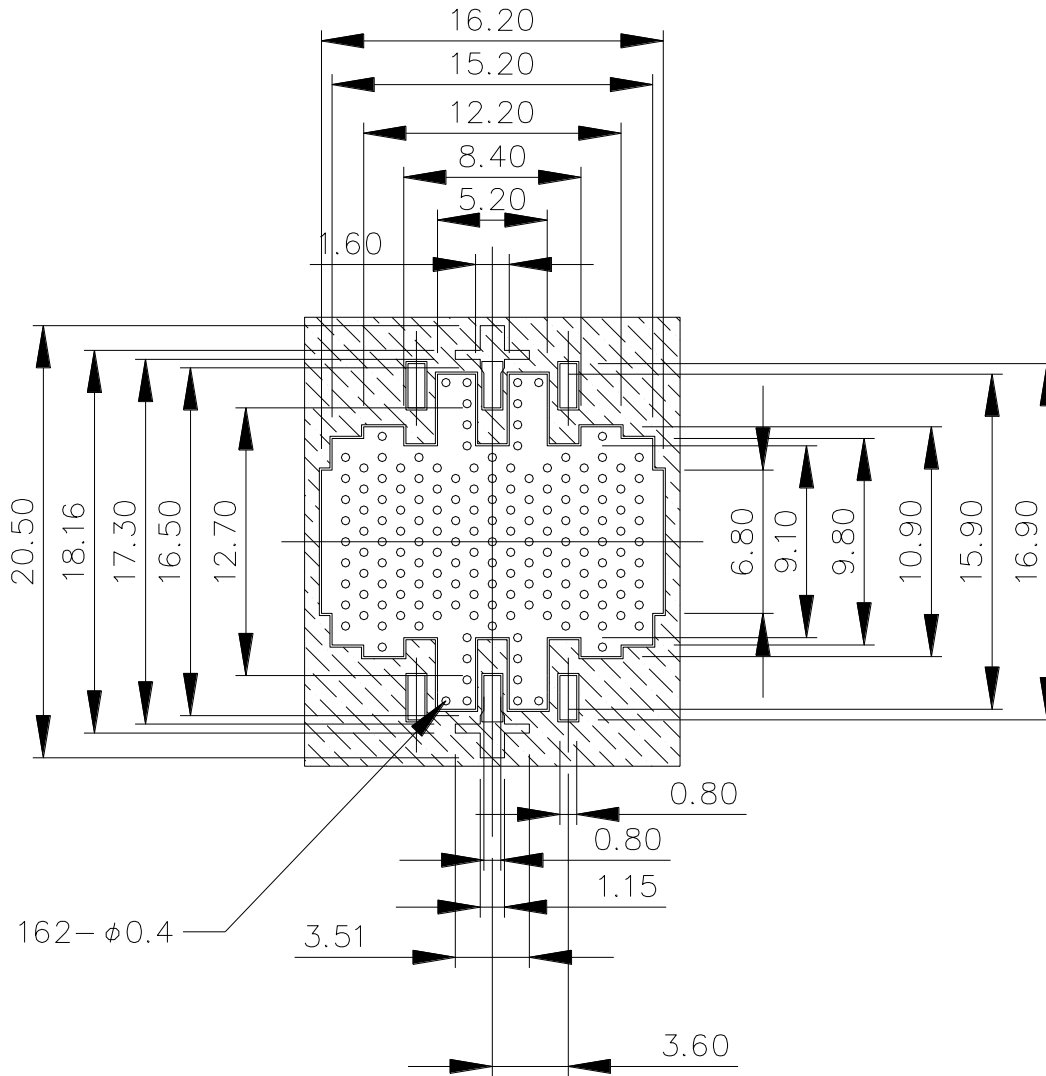
● S-Parameter



Frequency (MHz)	S11		S21		S12		S22	
	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
6900	0.588	129.7	3.036	-85.4	0.021	-68.3	0.519	134.1
7000	0.564	116.1	3.066	-99.1	0.021	-85.8	0.517	121.6
7100	0.536	102.0	3.163	-112.8	0.022	-102.4	0.511	110.6
7200	0.503	86.5	3.288	-127.1	0.024	-121.3	0.504	99.2
7300	0.458	68.9	3.387	-142.1	0.025	-142.5	0.480	87.9
7400	0.402	49.0	3.463	-157.9	0.025	-164.1	0.446	78.2
7500	0.349	26.2	3.551	-173.8	0.023	175.9	0.408	67.9
7600	0.311	-0.9	3.623	169.7	0.022	159.3	0.361	57.8
7700	0.301	-32.9	3.665	151.9	0.022	144.7	0.319	46.9
7800	0.323	-65.3	3.621	133.4	0.023	128.9	0.255	33.6
7900	0.370	-94.0	3.503	114.8	0.023	110.2	0.179	23.2
8000	0.421	-117.5	3.307	96.4	0.024	90.4	0.107	14.6
8100	0.471	-136.7	3.043	78.7	0.022	70.8	0.034	31.0



● Package Out line
Case Style : I2C



● PCB Pads and Solder-Resist Pattern

Notes :

1. Laminate : Rogers Corporation R04003, Thickness $t=0.508\text{mm}$, Cu Foil $18\mu\text{m}$.
Finish to copper foil : Ni $0.1\mu\text{m}$ min. / Au $0.1\mu\text{m}$ (Both side).
2.  : Resist

● Package Marking

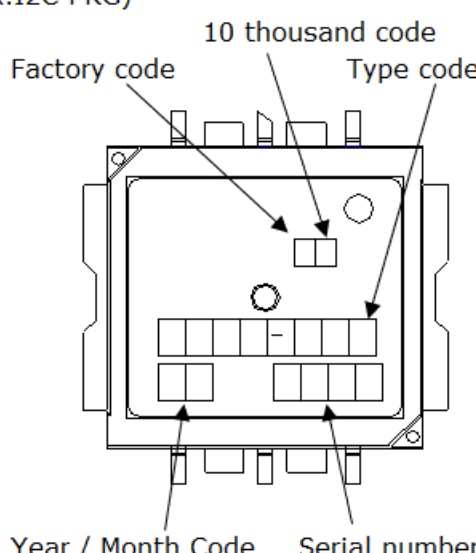
Lot Number : 1st: Year Code
 2nd: Month Code

Year Code

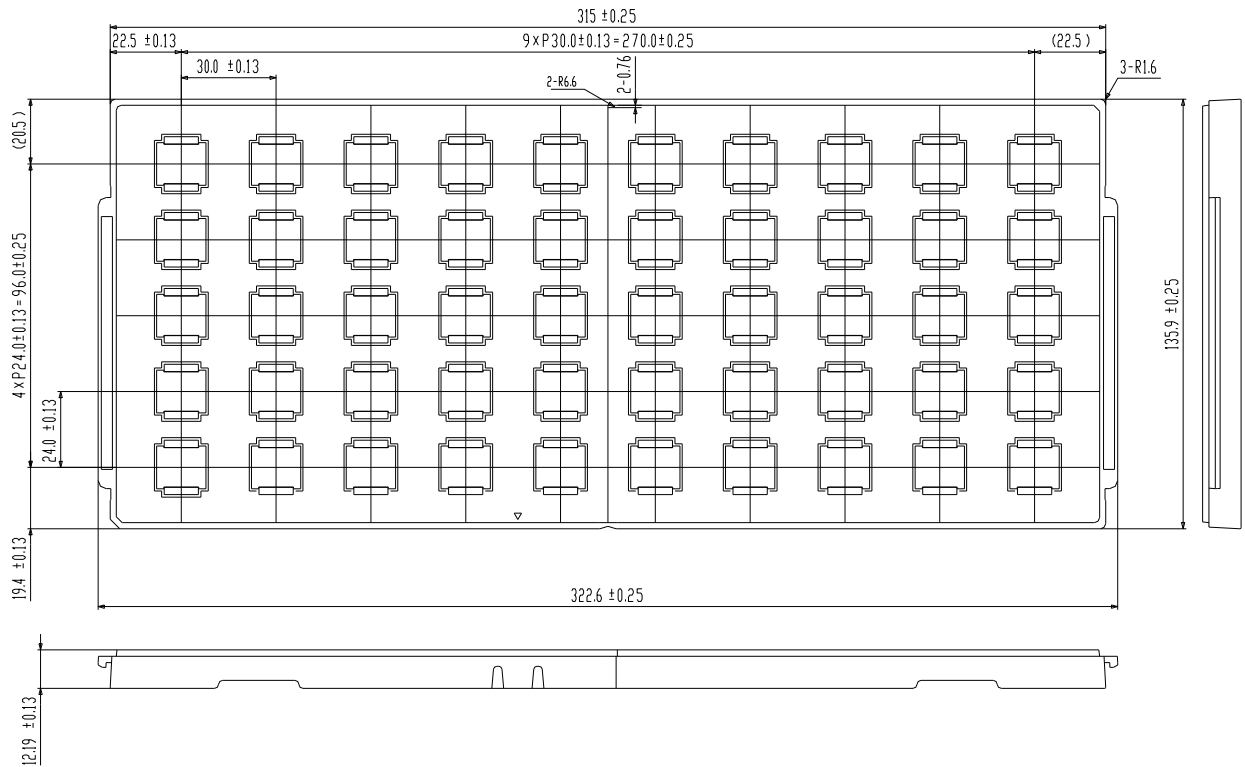
Code	Y	Z	A	B	C	D	E	F	G	H	I	J
Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027

Month Code

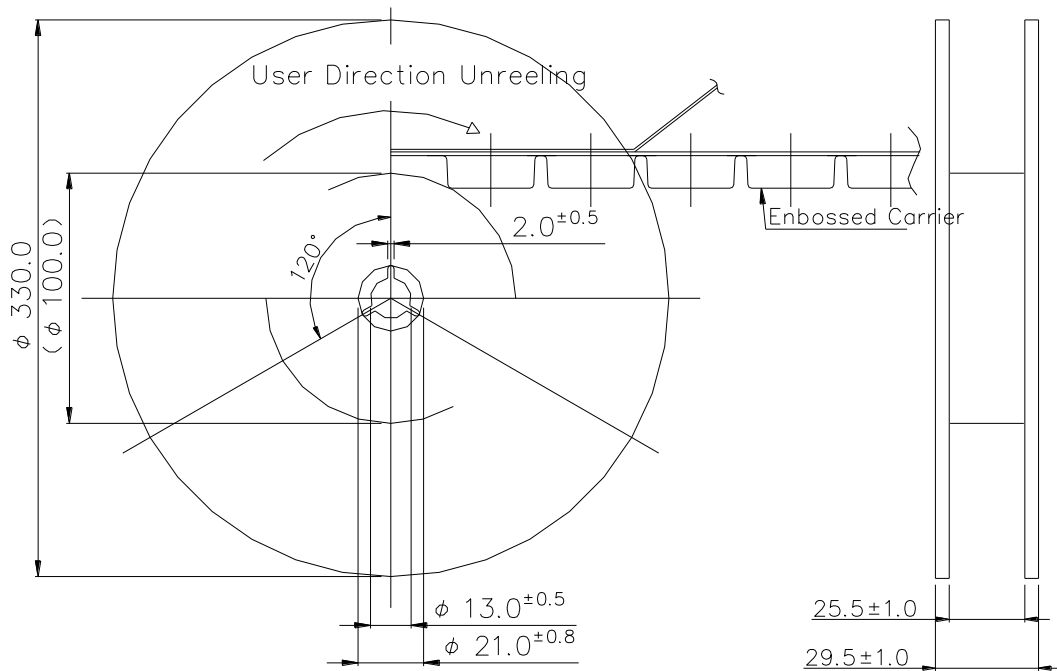
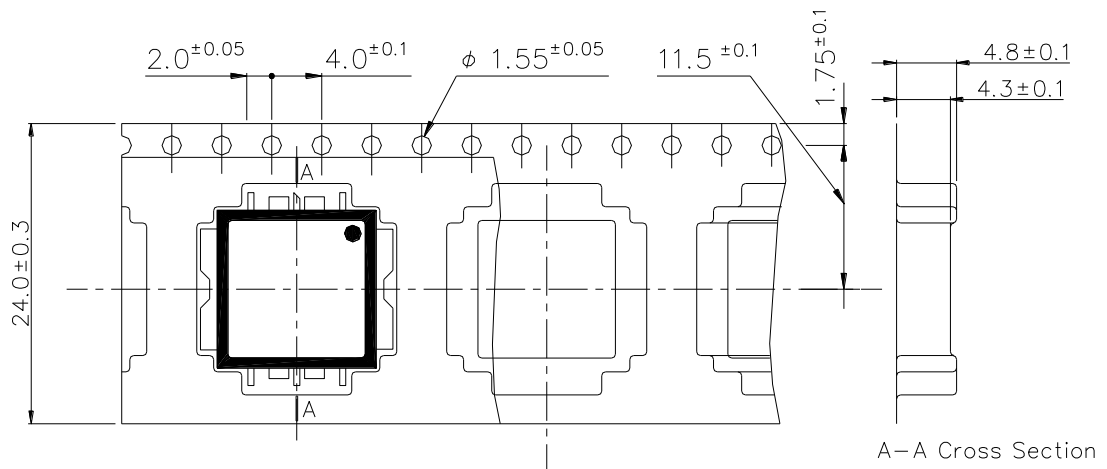
Code	H	M	N	P	R	S	T	U	W	X	Y	Z
Month	1	2	3	4	5	6	7	8	9	10	11	12

PKG	Marking	Type Number	Part Number
I2C	(ex.I2C PKG) 	ELM*****-**** SGK*****-**** ex. ELM5964-7PS SGK5872-20A	*****-**** *****-**** 5964-7PS 5872-20A

- **JEDEC Tray Dimension
(Part No:ELM7179-7PS)**



● **Tape/Reel Configuration**
(Part No:ELM7179-7PST)



Quantity: 500pcs/tape
Tape Material: Conductive PS

(unit in mm)

● Mounting Instructions for Package for Lead-free solder

Mounting Condition

For soldering, Lead-free solder (Sn-3.0Ag-0.5Cu)*1 or equivalent shall be used.

1. The example solder is a tin-rich alloy with 3.0% silver and 0.5% copper, often called Sn 96 for its approximate Tin content.
2. A rosin type flux with chlorine content of 0.2% or less shall be used. The rosin flux with low halogen content is recommended. When soldering, use the following time/ temperature profile with any of the methods listed for acceptable solder joints.
3. Make sure the devices have been properly prepared with flux prior soldering.

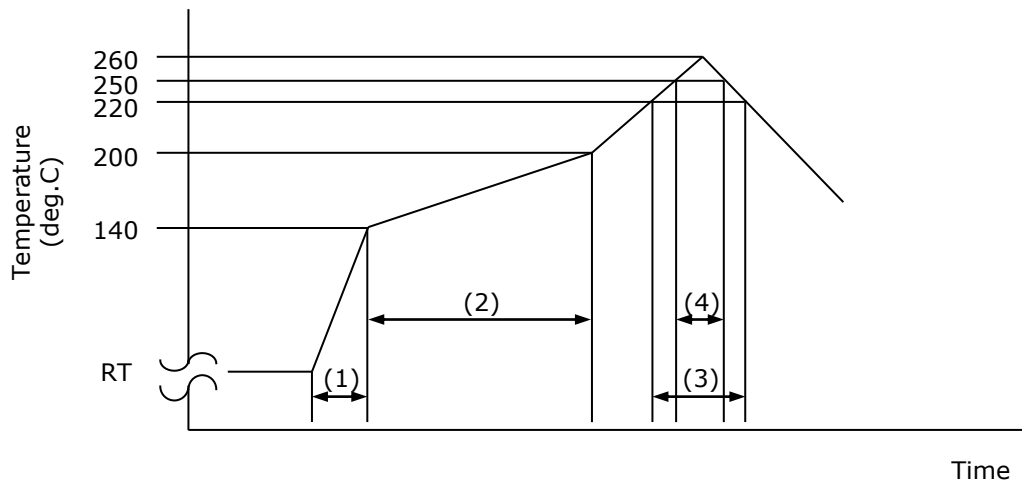
* Reflow soldering method (Infrared reflow / Heat circulation reflow / Hot plate reflow);

Limit solder to 3 reflow cycles because resin is used in the modules manufacturing process.

Excessive reflow will effect the resin resulting in a potential failure or latent defect.

The recommended reflow temperature profile is shown below. The temperature of the reflow profile must be measured at the device lead.

● Reflow temperature profile and condition:



- (1). Temperature rise: 3 deg.C/seconds.
 - (2). Preheating: 150 to 200 deg.C, 60 to 180seconds.
 - (3). Main heating: 220 deg.C, 60 seconds max.
 - (4). Main heating: 260 deg.C max., more than 250 deg.C, 20 to 40 seconds max.
- * Measurement point: Device Heat-sink (Source Pin).

1. The above-recommended conditions were confirmed using the manufacturer's equipment and materials. However, when soldering these products, the soldering condition should be verified by customer using their own particular equipment and materials.

● Cleaning

Avoid washing of the device after soldering by reflow method due to the risk of liquid absorption by the resin used in this part.

Humidity Lifetime for ELMxxxx-7PST

The following graph shows the effect of moisture on lifetime (moisture resistance) for the **ELMxxxx-7PST**. Each graph indicates the MTTF and failure rate prediction (Confidential Level = 90 %) which calculated from the results of highly accelerated temperature and humidity stress test (HAST).

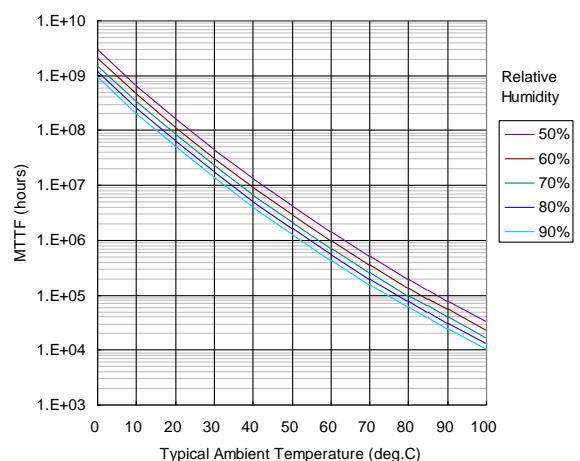
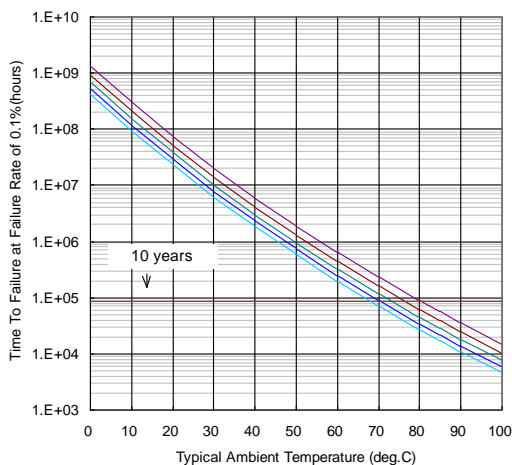
Representative of device type : ELM7179-7PST

Subject of device type : ELMxxxx-7PST

Field environmental conditions for operation

If the **ELMxxxx-7PST** is installed in a non-hermetic environment, please refer to the following recommendations and notes for design with, and assembly and use of our products.

- Note 1. When drain current cuts off, it should be cut off by drain bias, and not cut off by gate bias only. The humidity lifetime becomes shorter in case of the gate-only cut off operation due to electric field strength interacting with humidity.
- Note 2. **ELMxxxx-7PST** should be used under the environment conditions of no dew condensation. These plots do not apply in the case of liquid absorbed into the resin, whether applied to the part in assembly or as condensate in the application.



For Safety, Observe the Following Procedures Environmental Management

- Do not put this product into the mouth.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Respect all applicable laws of the country when discarding this product.
This product must be disposed in accordance with methods specified by applicable hazardous waste procedures.

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