0001



Features

• High Output Power: P1dB=36.0dBm (Typ.)

High Gain: G1dB=10.5dB (Typ.)

High Power Added Efficiency: PAE=35% (Typ.)

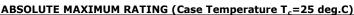
• Broad Band: Frequency=7.1 to 7.9GHz

· Internally Matched

Plastic Package for SMT applications

Description

The ELM7179-4PS is a power GaAs FET that is internally matched for standard communication bands to provide optimum power and gain.



ADSOLUTE MAXIMUM KATING (case reinperature r _c -2	s deg.c <i>)</i>	
Item	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	15	V
Gate-Source Voltage	V _{GS}	-5	V
Total Power Dissipation	P _T	27.3	W
Storage Temperature	T _{stg}	-40 to +125	deg.C
Channel Temperature	T _{ch}	175	deg.C

RECOMMENDED OPERATING CONDITION

Item	Symbol	Condition	Limit	Unit
Drain-Source Voltage	V_{DS}		<10	V
Forward Gate Current	I_{GF}	Rg=100ohm	<+16	mA
Reverse Gate Current	I_{GR}	Rg=100ohm	> -2.2	mA
Channel Temperature	T _{ch}		155	deg.C

ELECTRICAL CHARACTERISTICS (Case Temperature $T_c = 25 \text{ deg.C}$)

Item	Symbol	Condition		Limit			
	Syllibol	Condition	Min.	Тур.	Max.	Unit	
Saturated Drain Current	I _{DSS}	VDS=5V, VGS=0V	-	1700	2600	mA	
Trans Conductance	g _m	VDS=5V, IDS=1100mA	-	1700	-	mS	
Pinch-off Voltage	V_P	VDS=5V, IDS=85mA	-0.5	-1.5	-3.0	V	
Gate-Source Breakdown Voltage	V_{GSO}	IGS=-85uA	-5.0	-	-	V	
Output Power at 1dB G.C.P.	P_{1dB}		35.0	36.0	-	dBm	
Power Gain at 1dB G.C.P.	G _{1dB}	VDS=10V	9.5	10.5	-	dB	
Drain Current	I _{DSR}	Ids(DC)=1100mA(typ.)	-	1100	1300	mA	
Power Added Efficiency	PAE	f=7.1 to 7.9 GHz	-	35.0	-	%	
Gain Flatness	ΔG		-	-	1.2	dB	
3rd Order Inter Modulation Distortion	IM ₃	f=7.9GHz Δf=10MHz, 2-tone Test Pout=25.5dBm (S.C.L.)	-40.0	-43.0	-	dBc	
Thermal Resistance	R _{th}	Channel to Case	-	4.5	5.5	deg.C/W	
Channel Temperature Rise	ΔT_{ch}	$(V_{DS} \times I_{DSR} - Pout + Pin) \times R_{th}$	-	-	71.5	deg.C	

G.C.P.: Gain Compression Point, S.C.L.: Single Carrier Level

CASE STYLE	I2C	
RoHS Compliance	YES	
ESD	Class 3A	4000V to < 8000V
MSL	2	One year after opening the packing

Note: Based on ANSI/ESDA/JEDEC JS-001-2012(C=100pF, R=1.5kohm)

ELM7179-4PSC-Band Internally Matched FET

Ordering Information

Model Type	Model Type MOQ MOU		
ELM7179-4PS	15,00	No Limitation	50pcs-max./Tray ,
ELM/1/9-4P5	15pcs	No Limitation	1Tray-max./Packing
ELM7179-4PST	70. 4DCT E00ncc		24mm width Tape
ELM/1/9-4P51	500pcs	500pcs	(500pcs/Reel)

^{*} MOQ stands for Minimum Order Quantity.

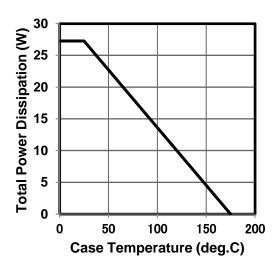
Note

- •This device will not be delivered with test data but tested pass/fail 100% against DC and RF specifications.
- •NO liquid cleaning process is suitable for this device. (including de-ionized water or solvent)

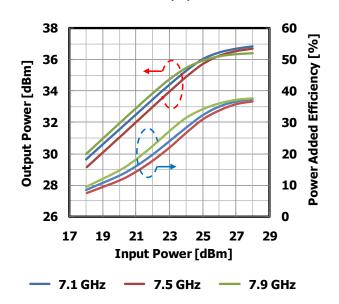
^{*} MOU stands for Minimum Order Unit size.



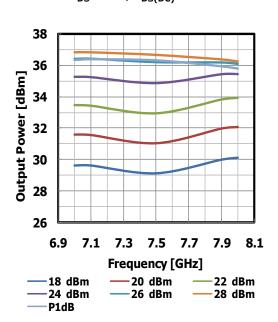
Power Derating Curve



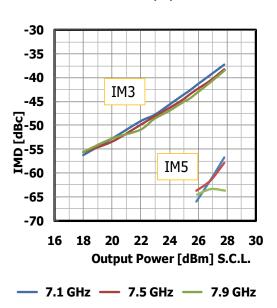
Input Power vs. Output Power and Power Added Efficiency V_{DS} =10V, $I_{DS(DC)}$ =1100mA



Output Power vs. Frequency V_{DS} =10V, $I_{DS(DC)}$ =1100mA

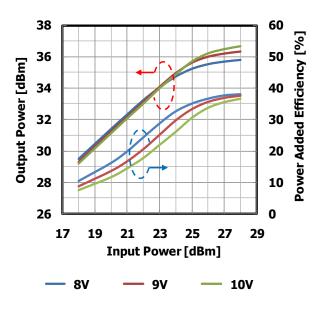


IMD vs. Output Power V_{DS} =10V, $I_{DS(DC)}$ =1100mA

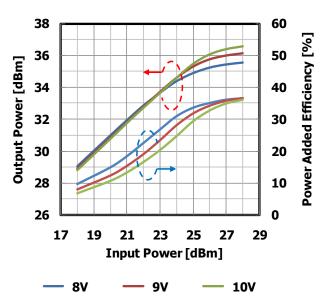




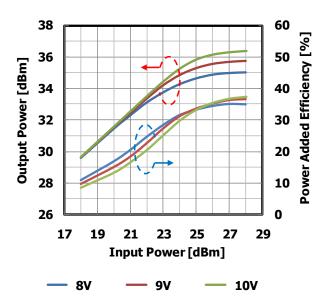
Input Power vs. Output Power, Power Added Efficiency by Drain Voltage IDS(DC)=1100mA @7.1GHz



Input Power vs. Output Power, Power Added Efficiency by Drain Voltage IDS(DC)=1100mA @7.5GHz

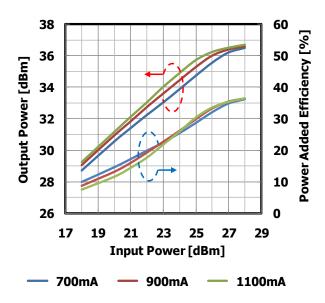


Input Power vs. Output Power, Power Added Efficiency by Drain Voltage IDS(DC)=1100mA @7.9GHz

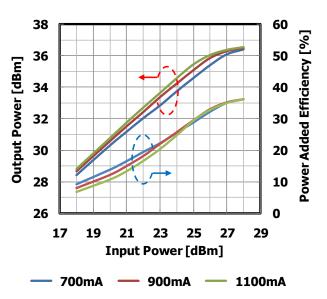




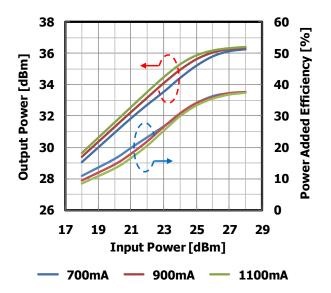
Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current VDS=10V @7.1GHz



Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current VDS=10V @7.5GHz

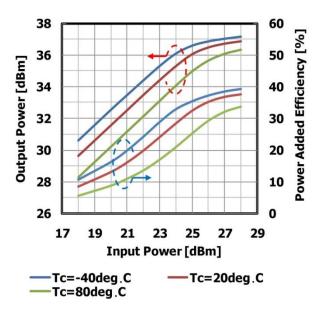


Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current VDS=10V @7.9GHz

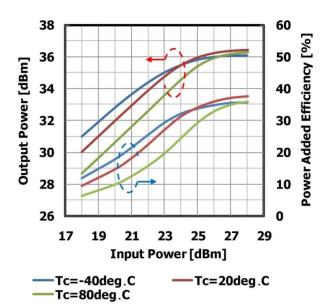




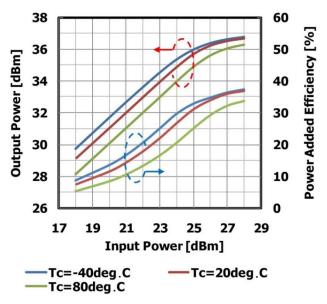
Input Power vs. Output Power, Power Added Efficiency by Temperature VDS=10V @7.1GHz



Input Power vs. Output Power, Power Added Efficiency by Temperature VDS=10V @7.9GHz

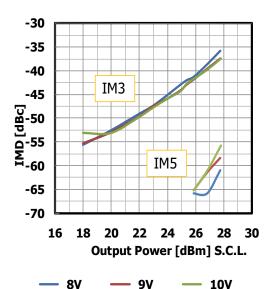


Input Power vs. Output Power, Power Added Efficiency by Temperature VDS=10V @7.5GHz



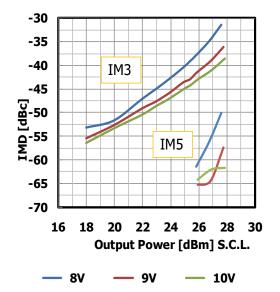


IMD Performance vs. Output Power by Drain Voltage IDS(DC)=1100mA @7.1GHz

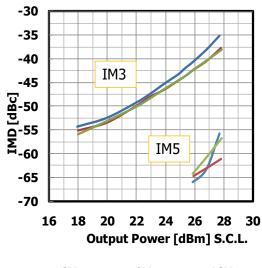


IMD Performance vs. Output Power

by Drain Voltage
IDS(DC)=1100mA @7.9GHz

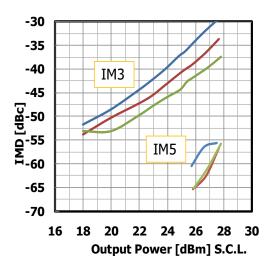


IMD Performance vs. Output Power by Drain Voltage IDS(DC)=1100mA @7.5GHz



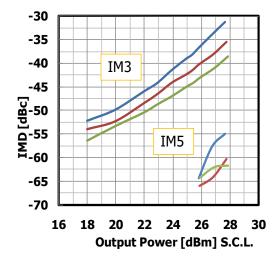


IMD Performance vs. Output Power by Quiescent Drain Current VDS=10V @7.1GHz



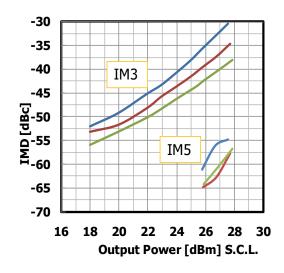
— 700mA — 900mA — 1100mA

IMD Performance vs. Output Power by Quiescent Drain Current VDS=10V @7.9GHz



— 700mA ─ 900mA ─ 1100mA

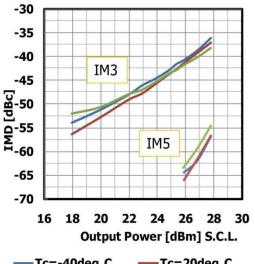
IMD Performance vs. Output Power by Quiescent Drain Current VDS=10V @7.5GHz



— 700mA — 900mA — 1100mA

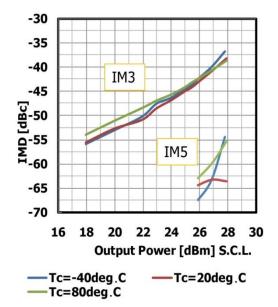


IMD Performance vs. Output Power by Temperature VDS=10V @7.1GHz

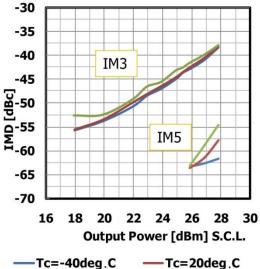


—Tc=-40deg.C —Tc=20deg.C —Tc=80deg.C

IMD Performance vs. Output Power by Temperature VDS=10V @7.9GHz



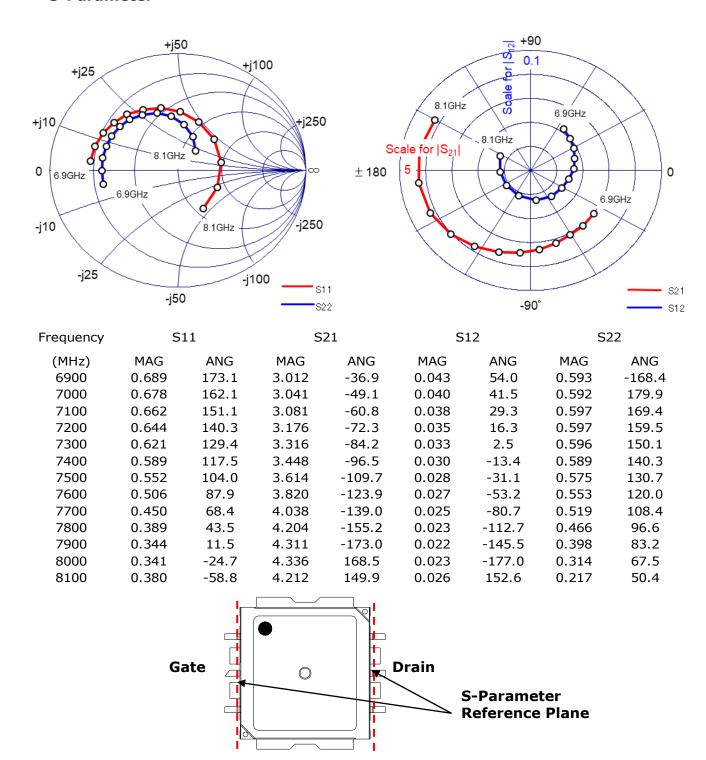
IMD Performance vs. Output Power by Temperature VDS=10V @7.5GHz



—Tc=-40deg.C —Tc=20deg.C —Tc=80deg.C

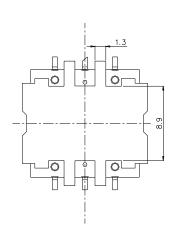


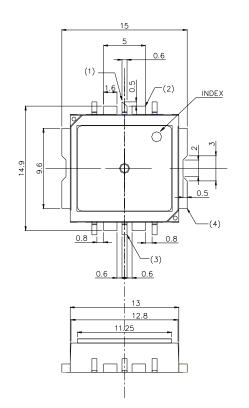
• S-Parameter

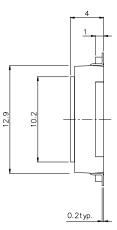




Package Out line Case Style : I2C







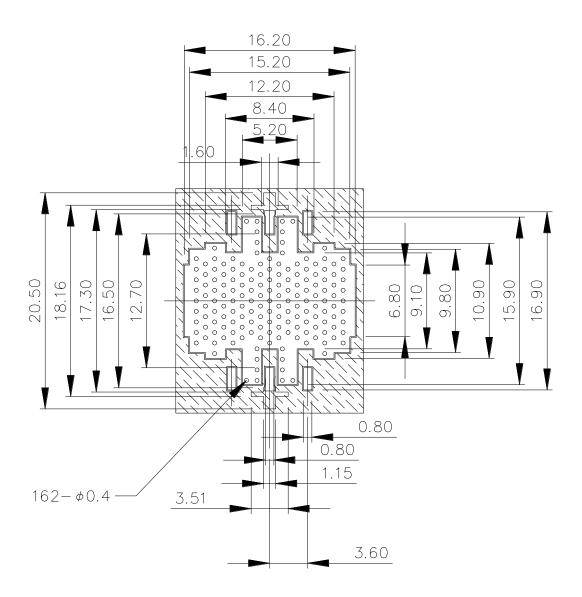
- 1. Gate
- 2. Source
- 3. Drain
- 4. Source

Tolerance: ±0.15

Unit: mm



• PCB Pads and Solder-Resist Pattern



Notes:

1. Laminate: Rogers Corporation RO4003, Thickness t=0.508mm, Cu Foil 18um. Finish to copper foil: Ni 0.1um min. / Au 0.1um (Both side).

2. Resist



• Package Marking

Lot Number : 1st: Year Code

2nd: Month Code

Year Code

Code	Υ	Z	Α	В	С	D	Е	F	G	Н	I	J
Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027

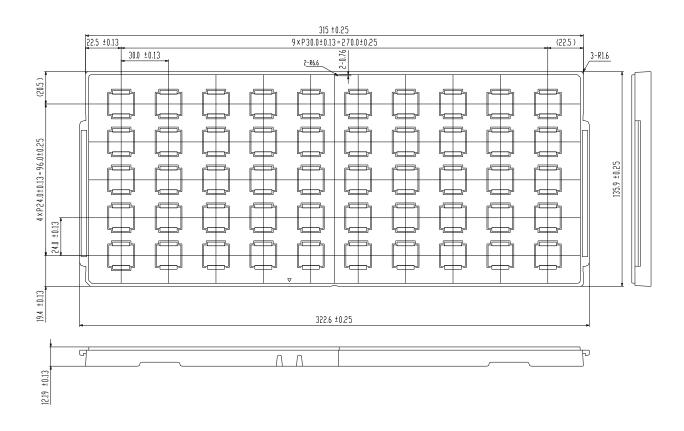
Month Code

Code	Н	М	N	Р	R	S	Т	U	W	Х	Υ	Z
Month	1	2	3	4	5	6	7	8	9	10	11	12

PKG Marking Type Nunber Part Numb I2C (ex.I2C PKG)	ar
I2C (ex I2C PKG)	51
10 thousand code Factory code Type code ELM****-*** SGK****-*** ex. ELM5964-7PS SGK5872-20A Year / Month Code Serial number	

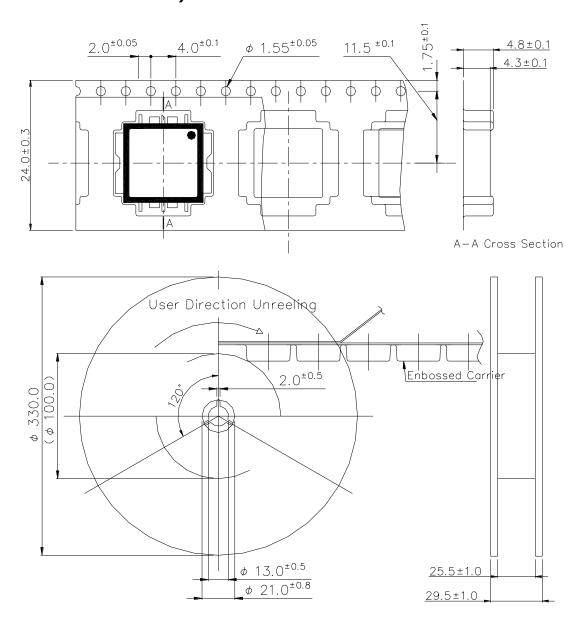


JEDEC Tray Dimension (Part No:ELM7179-4PS)





Tape/Reel Configuration (Part No:ELM7179-4PST)



Quantity: 500pcs/tape

Tape Material: Conductive PS

(unit in mm)



• Mounting Instructions for Package for Lead-free solder

Mounting Condition

For soldering, Lead-free solder (Sn-3.0Ag-0.5Cu)*1 or equivalent shall be used.

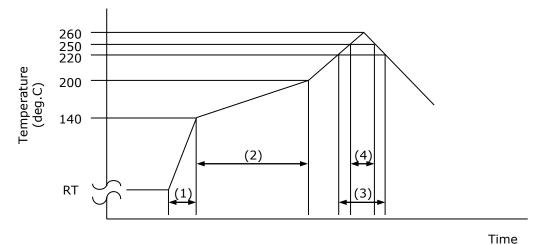
- 1. The example solder is a tin-rich alloy with 3.0% silver and 0.5% copper, often called Sn 96 for its approximate Tin content.
- 2. A rosin type flux with chlorine content of 0.2% or less shall be used. The rosin flux with low halogen content is recommended. When soldering, use the following time/ temperature profile with any of the methods listed for acceptable solder joints.
- 3. Make sure the devices have been properly prepared with flux prior soldering.

* Reflow soldering method (Infrared reflow / Heat circulation reflow / Hot plate reflow);

Limit solder to 3 reflow cycles because resin is used in the modules manufacturing process. Excessive reflow will effect the resin resulting in a potential failure or latent defect.

The recommended reflow temperature profile is shown below. The temperature of the reflow profile must be measured at the device lead.

• Reflow temperature profile and condition:



(1). Temperature rise: 3 deg.C/seconds.

(2). Preheating: 150 to 200 deg.C, 60 to 180seconds.

(3). Main heating: 220 deg.C, 60 seconds max.

(4). Main heating: 260 deg.C max., more than 250 deg.C, 20 to 40 seconds max.

* Measurement point: Device Heat-sink (Source Pin).

1. The above-recommended conditions were confirmed using the manufacturer's equipment and materials. However, when soldering these products, the soldering condition should be verified by customer using their own particular equipment and materials.

Cleaning

Avoid washing of the device after soldering by reflow method due to the risk of liquid absorption by the resin used in this part.



• Humidity Lifetime and fit rate for ELMxxxx-4PST

The following graph shows the effect of moisture on lifetime (moisture resistance) for the ELMxxxx-4PST. Each graph indicates the MTTF and failure rate prediction (Confidential Level = 90 %) which calculated from the results of highly accelerated temperature and humidity stress test (HAST).

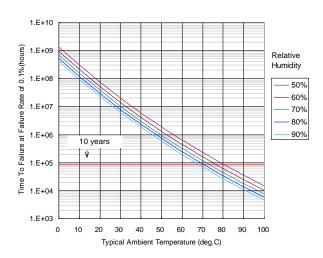
Representative of device type : ELM7179-4PST

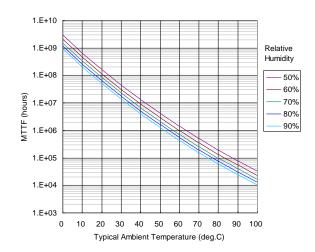
Subject of device type : ELMxxxx-4PST

Field environmental conditions for operation

If the **ELMxxxx-4PST** is installed in a non-hermetic environment, please refer to the following recommendations and notes for design with, and assembly and use of our products.

- Note 1. When drain current cuts off, it should be cut off by drain bias, and not cut off by gate bias only. The humidity lifetime becomes shorter in case of the gate-only cut off operation due to electric field strength interacting with humidity.
- Note 2. **ELMxxx-4PST** should be used under the environment conditions of no dew condensation. These plots do not apply in the case of liquid absorbed into the resin, whether applied to the part in assembly or as condensate in the application.







For Safety, Observe the Following Procedures Environmental Management

- Do not put this product into the mouth.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Respect all applicable laws of the country when discarding this product.
 This product must be disposed in accordance with methods specified by applicable hazardous waste procedures.

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Jun. 2020

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