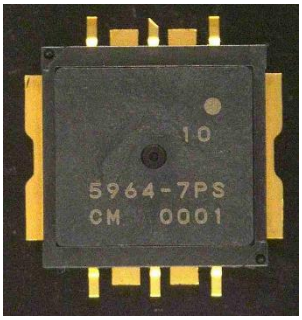
■ Features

- High Output Power: P1dB=39.0dBm (Typ.)
- High Gain: G1dB=11.0dB (Typ.)
- High Power Added Efficiency : PAE=36% (Typ.)
- Broad Band: Frequency= 5.9 to 6.4GHz
- Internally Matched
- Plastic Package for SMT applications



■ Description

The ELM5964-7PS is a power GaAs FET that is internally matched for standard communication bands to provide optimum power and gain.

ABSOLUTE MAXIMUM RATING (Case Temperature T_c=25 deg.C)

Item	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	15	V
Gate-Source Voltage	V_{GS}	-5	V
Total Power Dissipation	P_T	50	W
Storage Temperature	T_{stg}	-40 to +125	deg.C
Channel Temperature	T_{ch}	175	deg.C

RECOMMENDED OPERATING CONDITION

Item	Symbol	Condition	Limit	Unit
Drain-Source Voltage	V_{DS}		<10	V
Forward Gate Current	I_{GF}	Rg=100ohm	<+16	mA
Reverse Gate Current	I_{GR}	Rg=100ohm	> -2.2	mA
Channel Temperature	T_{ch}		155	deg.C

ELECTRICAL CHARACTERISTICS (Case Temperature T_c=25 deg.C)

Item	Symbol	Condition	Limit			Unit
			Min.	Typ.	Max.	
Saturated Drain Current	I_{DSS}	VDS=5V, VGS=0V	-	3400	5200	mA
Trans Conductance	g_m	VDS=5V, IDS=2200mA	-	3400	-	mS
Pinch-off Voltage	V_P	VDS=5V, IDS=170mA	-0.5	-1.5	-3.0	V
Gate-Source Breakdown Voltage	V_{GSO}	IGS=-170uA	-5.0	-	-	V
Output Power at 1dB G.C.P.	P_{1dB}	VDS=10V Ids(DC)=2200mA(typ.) f=5.9 to 6.4 GHz	38.0	39.0	-	dBm
Power Gain at 1dB G.C.P.	G_{1dB}		9.5	11.0	-	dB
Drain Current	I_{DSR}		-	2200	2600	mA
Power Added Efficiency	PAE		-	36.0	-	%
Gain Flatness	ΔG		-	-	1.2	dB
3rd Order Inter Modulation Distortion	IM_3	f=6.4GHz Δf=10MHz, 2-tone Test Pout=28.0dBm (S.C.L.)	-40.0	-45.0	-	dBc
Thermal Resistance	R_{th}	Channel to Case	-	2.5	3.0	deg.C/W
Channel Temperature Rise	ΔT_{ch}	(V_{DS} x I_{DSR} – Pout + Pin) x R_{th}	-	-	80.0	deg.C

G.C.P. : Gain Compression Point, S.C.L. : Single Carrier Level

CASE STYLE	I2C	
RoHS Compliance	YES	
ESD	Class 3A	4000V to < 8000V
MSL	2	One year after opening the packing

Note : Based on ANSI/ESDA/JEDEC JS-001-2012(C=100pF, R=1.5kohm)

Ordering Information

Model Type	MOQ	MOU	Packing Style
ELM5964-7PS	15pcs	No Limitation	50pcs-max./Tray , 1Tray-max./Packing
ELM5964-7PST	500pcs	500pcs	24mm width Tape (500pcs/Reel)

* MOQ stands for Minimum Order Quantity.
* MOU stands for Minimum Order Unit size.

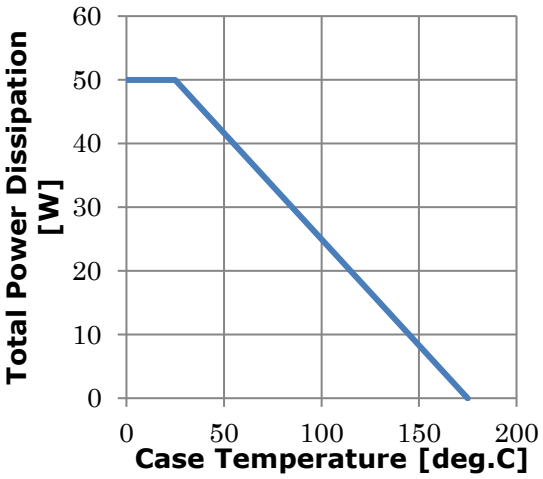
Note

・This device will not be delivered with test data but tested pass/fail 100% against DC and RF specifications.

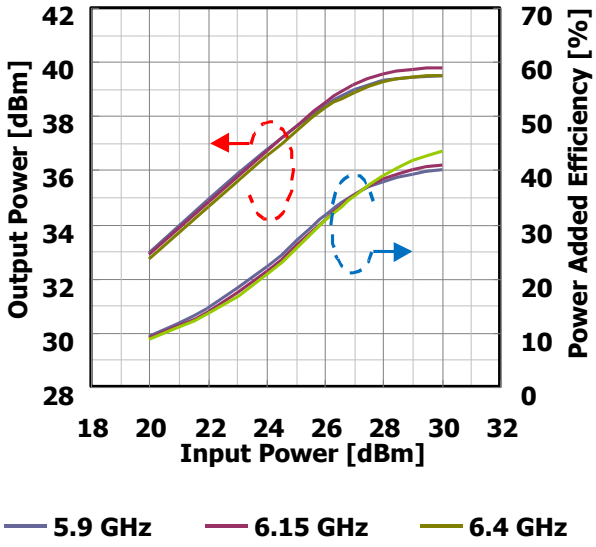
・NO liquid cleaning process is suitable for this device. (including de-ionized water or solvent)

● RF Characteristics

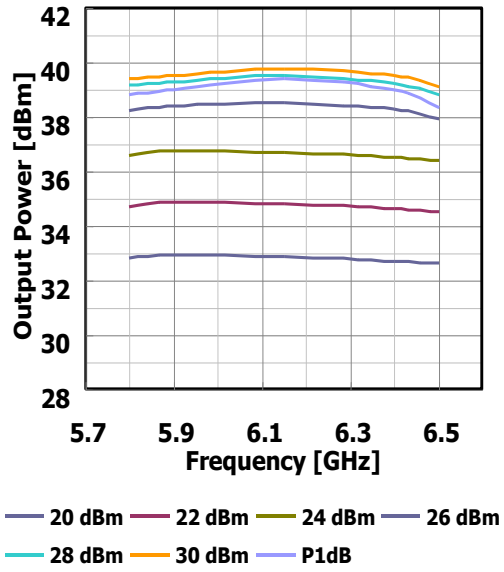
Power Derating Curve



Input Power vs. Output Power and Power Added Efficiency
V_{DS}=10V, $I_{DS(DC)}$=2200mA



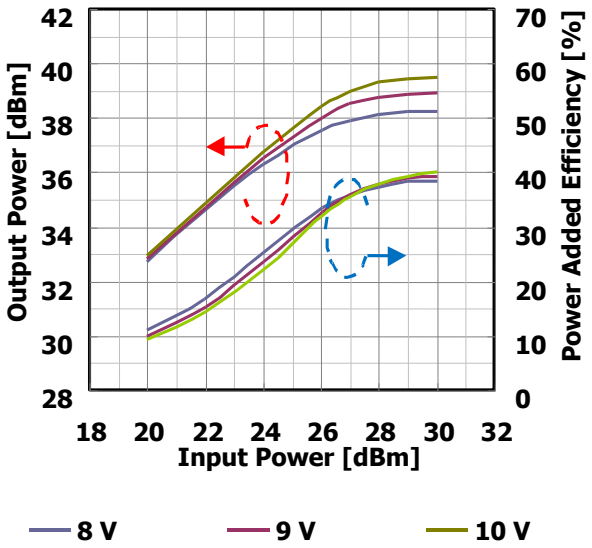
Output Power vs. Frequency
V_{DS}=10V, $I_{DS(DC)}$=2200mA



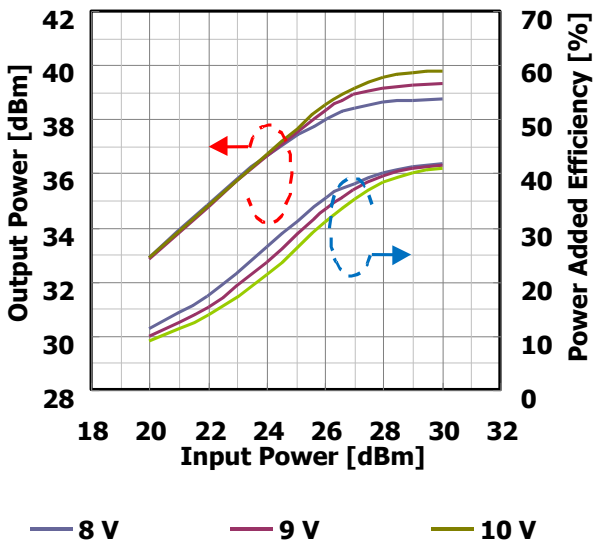
IMD vs. Output Power
V_{DS}=10V, $I_{DS(DC)}$=2200mA

RF Characteristics

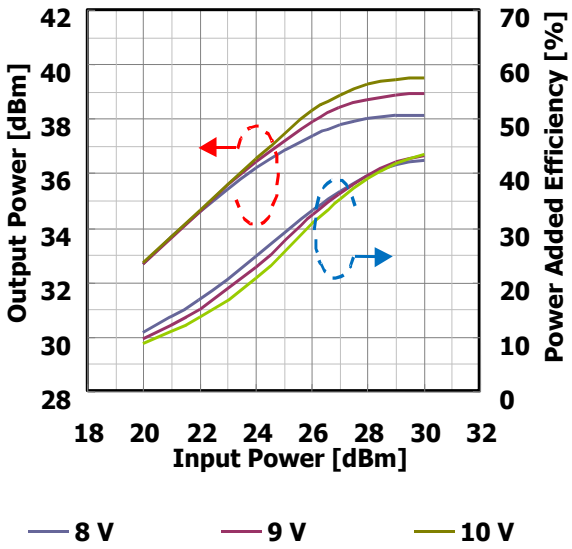
Input Power vs. Output Power, Power Added Efficiency by Drain Voltage IDS(DC)=2200mA @5.9GHz



Input Power vs. Output Power, Power Added Efficiency by Drain Voltage IDS(DC)=2200mA @6.15GHz

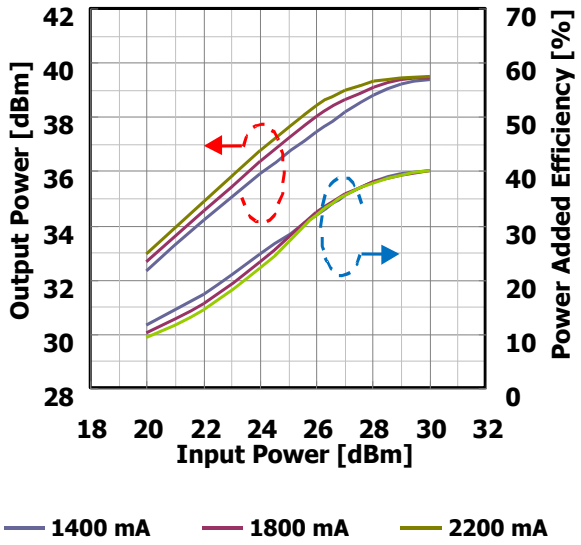


Input Power vs. Output Power, Power Added Efficiency by Drain Voltage IDS(DC)=2200mA @6.4GHz

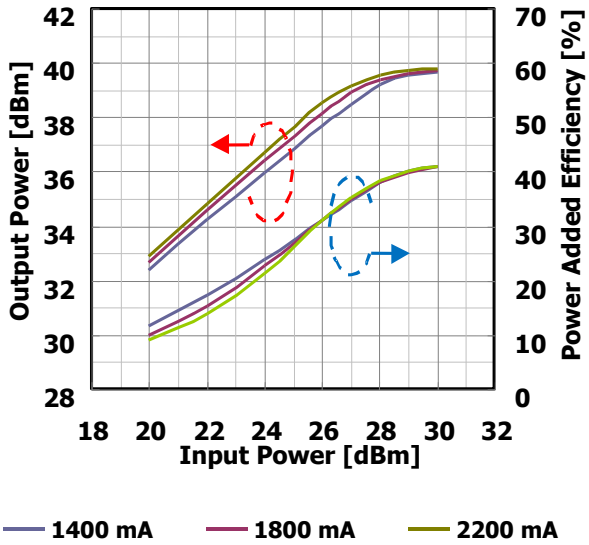


● RF Characteristics

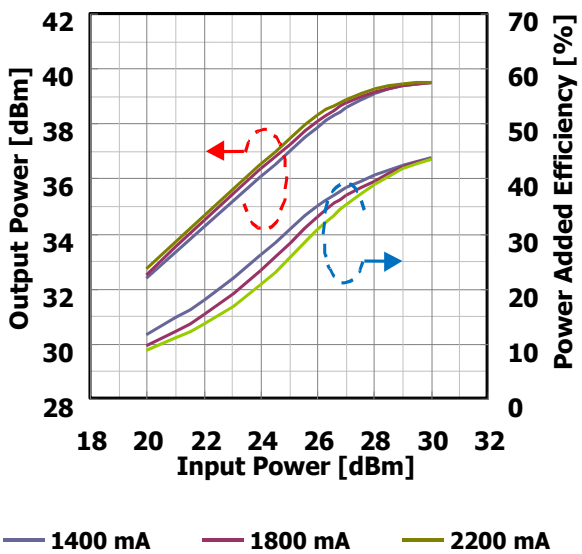
Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current VDS=10V @5.9GHz



Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current VDS=10V @6.15GHz

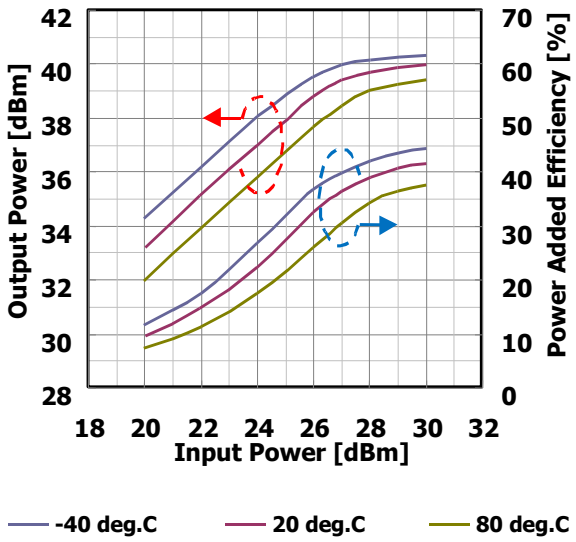


Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current VDS=10V @6.4GHz

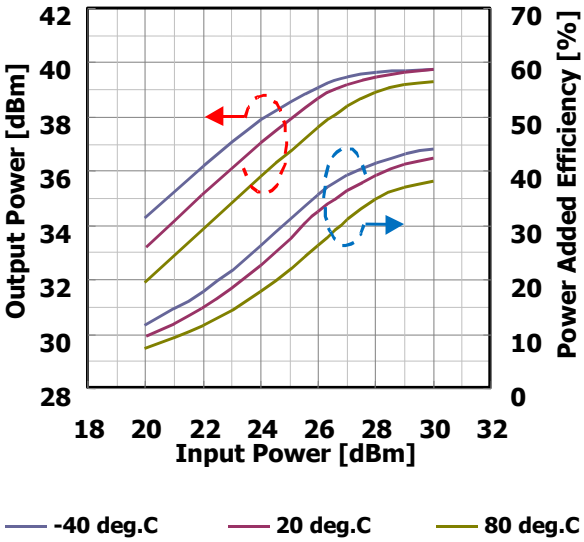


● RF Characteristics

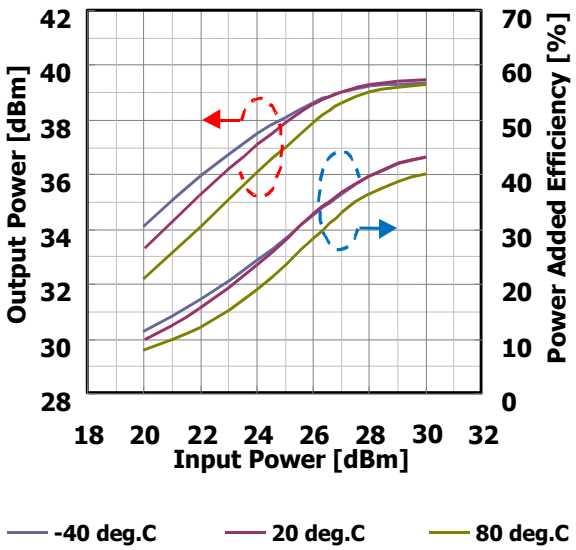
Input Power vs. Output Power, Power Added Efficiency by Case Temperature VDS=10V IDS(DC)=2200mA @5.9GHz



Input Power vs. Output Power, Power Added Efficiency by Case Temperature VDS=10V IDS(DC)=2200mA @6.15GHz

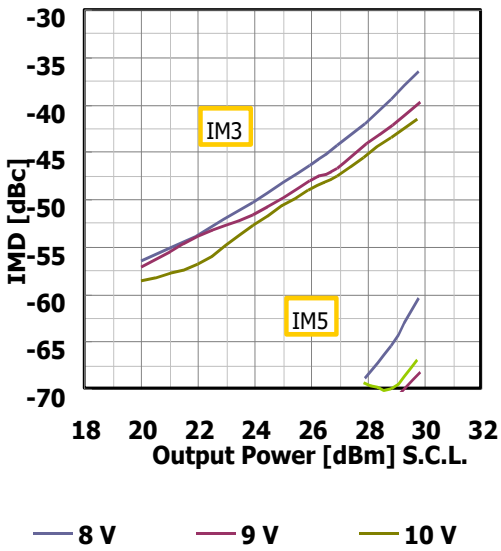


Input Power vs. Output Power, Power Added Efficiency by Case Temperature VDS=10V IDS(DC)=2200mA @6.4GHz

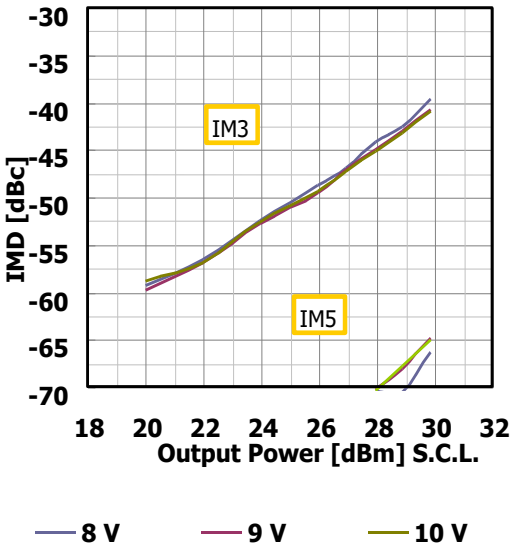


● RF Characteristics

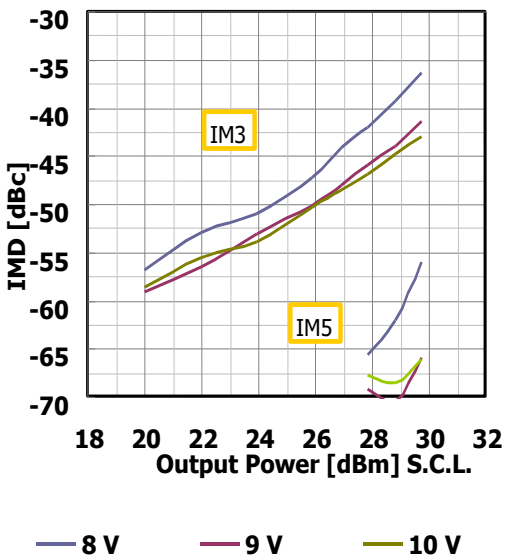
IMD Performance vs. Output Power by Drain Voltage IDS(DC)=2200mA @5.9GHz



IMD Performance vs. Output Power by Drain Voltage IDS(DC)=2200mA @6.15GHz

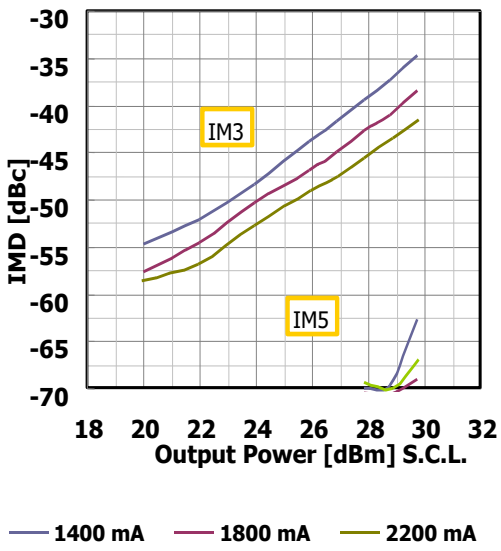


IMD Performance vs. Output Power by Drain Voltage IDS(DC)=2200mA @6.4GHz

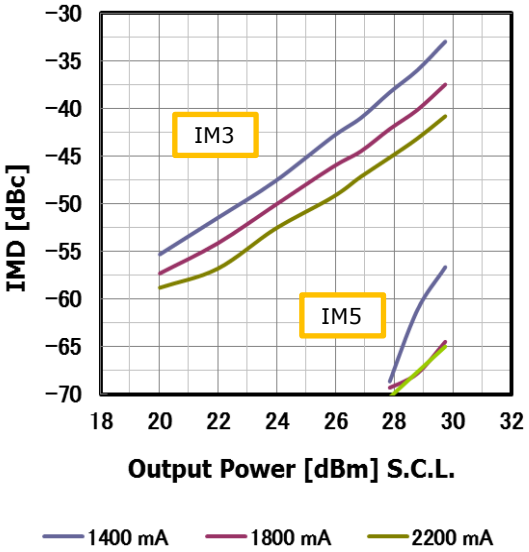


● RF Characteristics

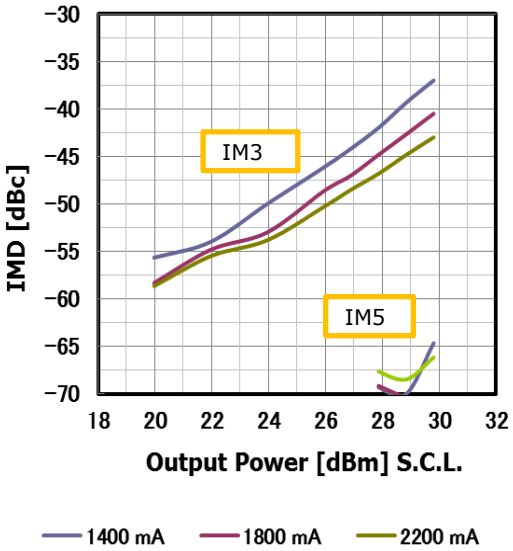
IMD Performance vs. Output Power by Quiescent Drain Current VDS=10V @5.9GHz



IMD Performance vs. Output Power by Quiescent Drain Current VDS=10V @6.15GHz

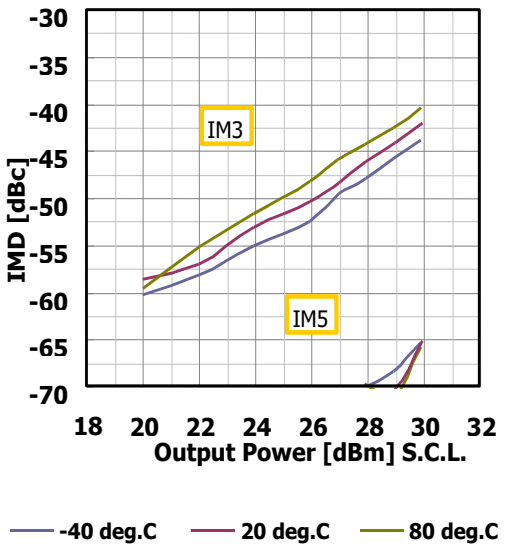


IMD Performance vs. Output Power by Quiescent Drain Current VDS=10V @6.4GHz

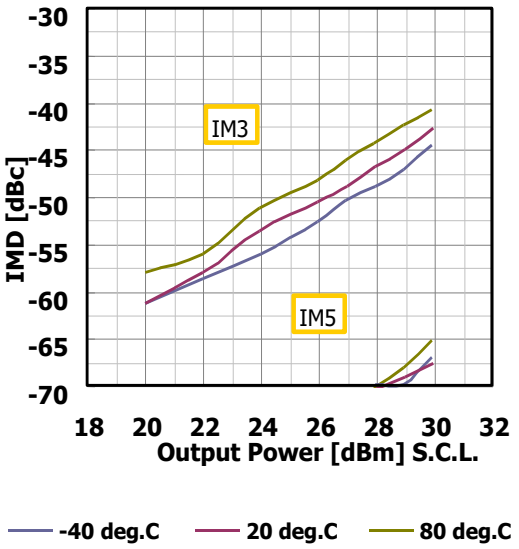


● RF Characteristics

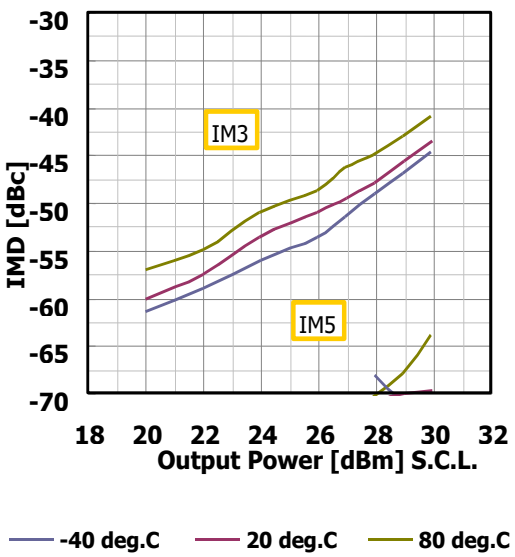
IMD Performance vs. Output Power by Case Temperature VDS=10V IDS(DC)=2200mA @5.9GHz



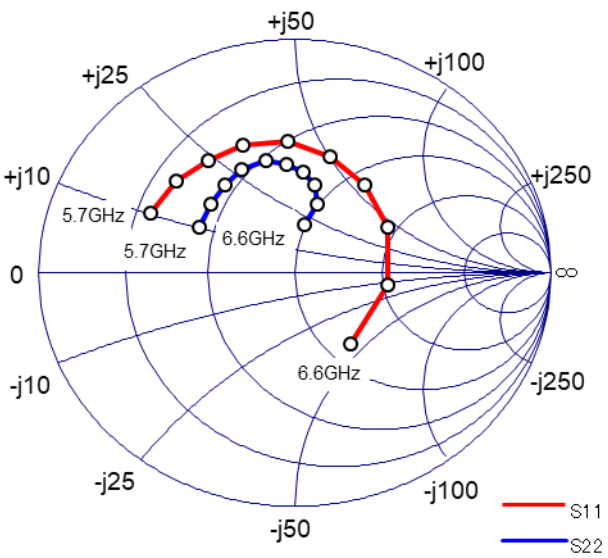
IMD Performance vs. Output Power by Case Temperature VDS=10V IDS(DC)=2200mA @6.15GHz

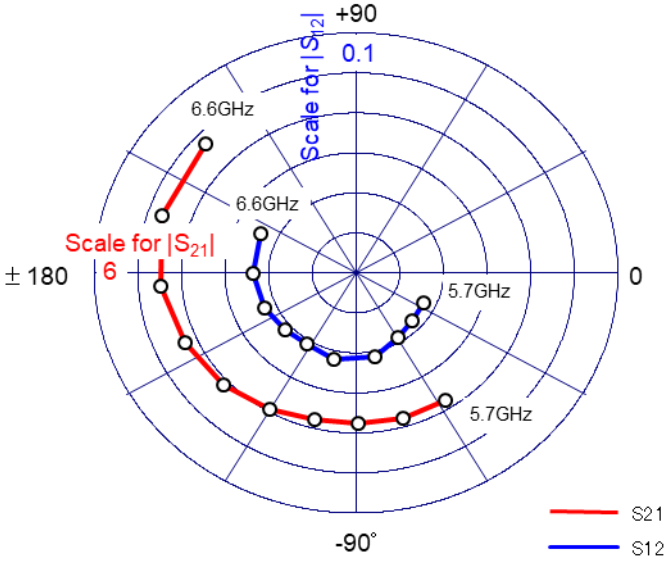


IMD Performance vs. Output Power by Case Temperature VDS=10V IDS(DC)=2200mA @6.4GHz

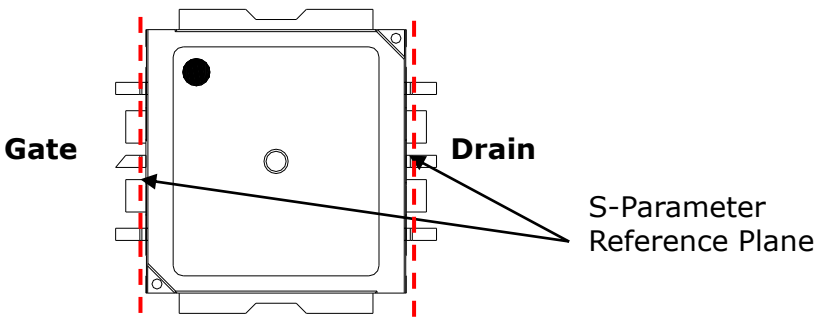


● S-Parameter

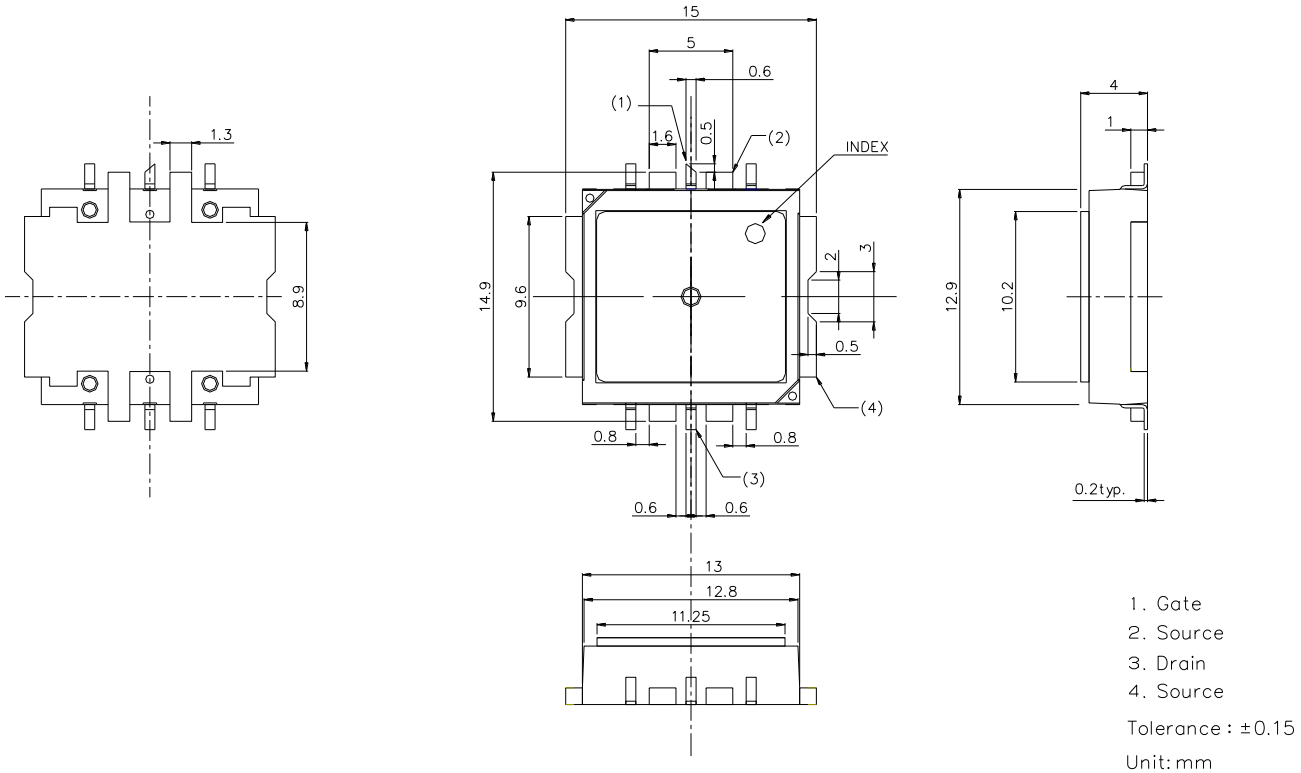




Frequency	S11		S21		S12		S22	
(MHz)	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
5700	0.618	155.6	3.778	-57.4	0.029	-25.9	0.42	152.6
5800	0.607	139.7	3.782	-73.6	0.029	-43.4	0.442	138.1
5900	0.589	124.9	3.773	-89.3	0.031	-59.7	0.463	126
6000	0.582	110	3.789	-104.3	0.035	-78.1	0.486	114.9
6100	0.567	92.6	3.953	-120.1	0.037	-103.2	0.494	103.1
6200	0.519	74.3	4.12	-137.4	0.035	-122.3	0.467	93.6
6300	0.467	54	4.297	-155.9	0.036	-138.8	0.434	85.6
6400	0.413	28	4.485	-175.7	0.038	-157.7	0.384	78.2
6500	0.367	-8.1	4.678	162	0.04	-179.5	0.307	73.2
6600	0.374	-54.7	4.72	137.1	0.04	156	0.209	79.3



- **Package Out line**
  Case Style : I2C

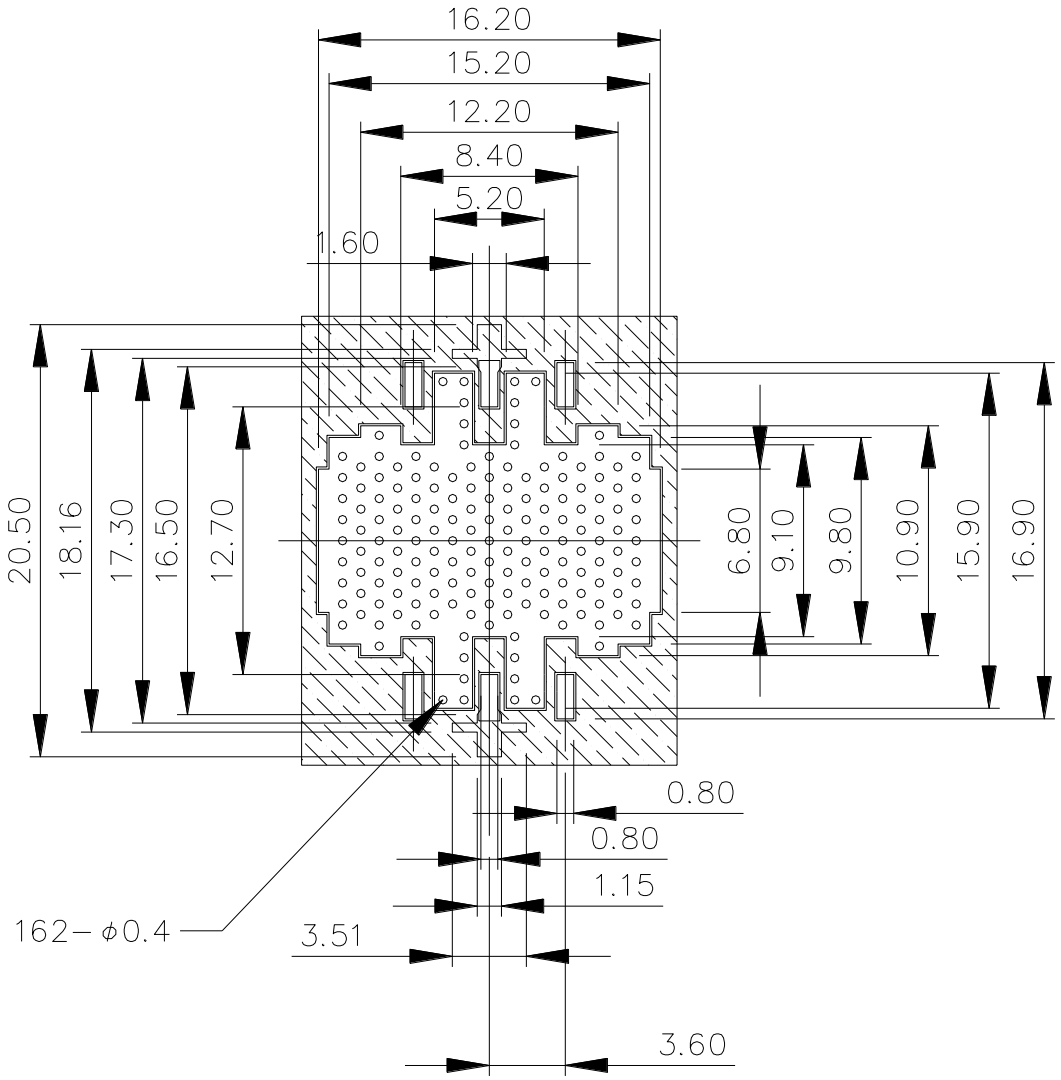


1. Gate
2. Source
3. Drain
4. Source

Tolerance : ±0.15

Unit: mm

- **PCB Pads and Solder-Resist Pattern**



Notes :
1. Laminate : Rogers Corporation RO4003, Thickness t=0.508mm, Cu Foil 18um.
   Finish to copper foil : Ni 0.1um min. / Au 0.1um (Both side).
2. ▨ : Resist

● Package Marking

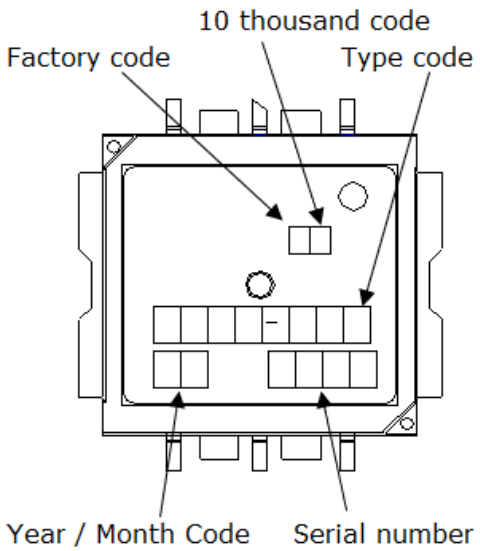
Lot Number : 1st: Year Code
2nd: Month Code

Year Code

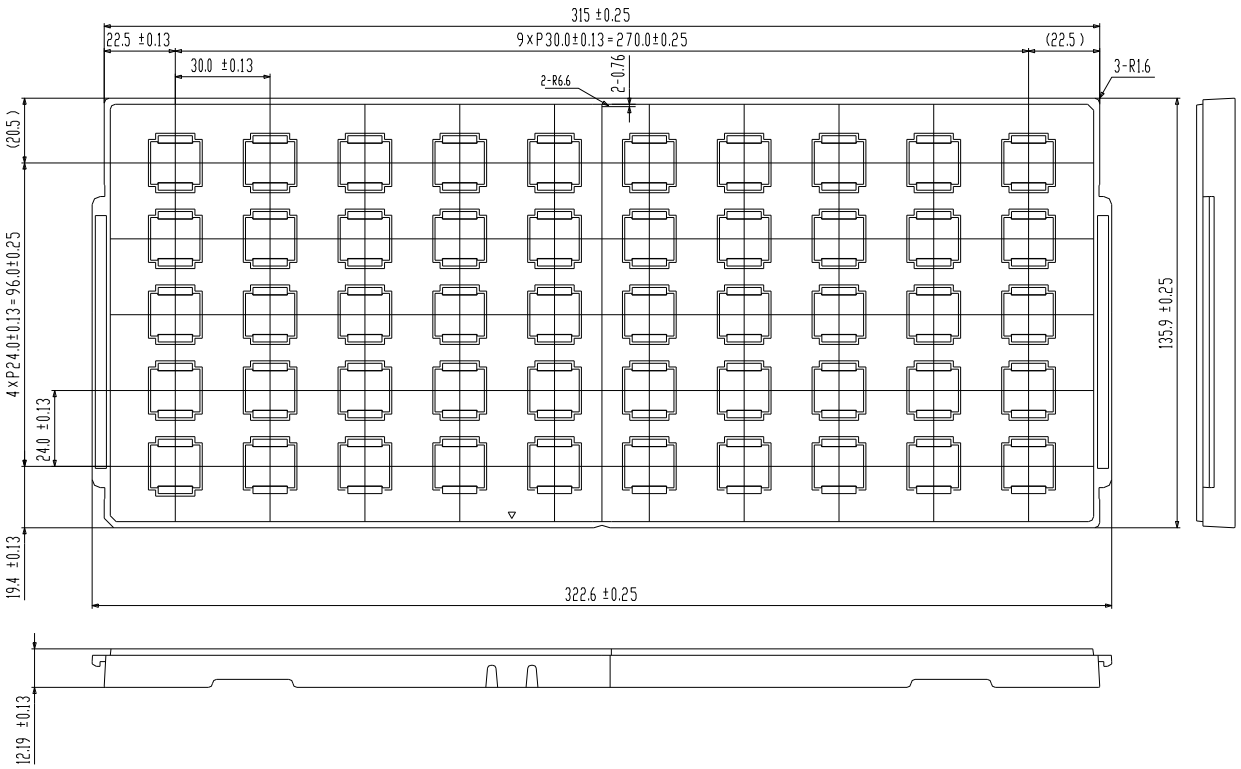
Code	Y	Z	A	B	C	D	E	F	G	H	I	J
Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027

Month Code

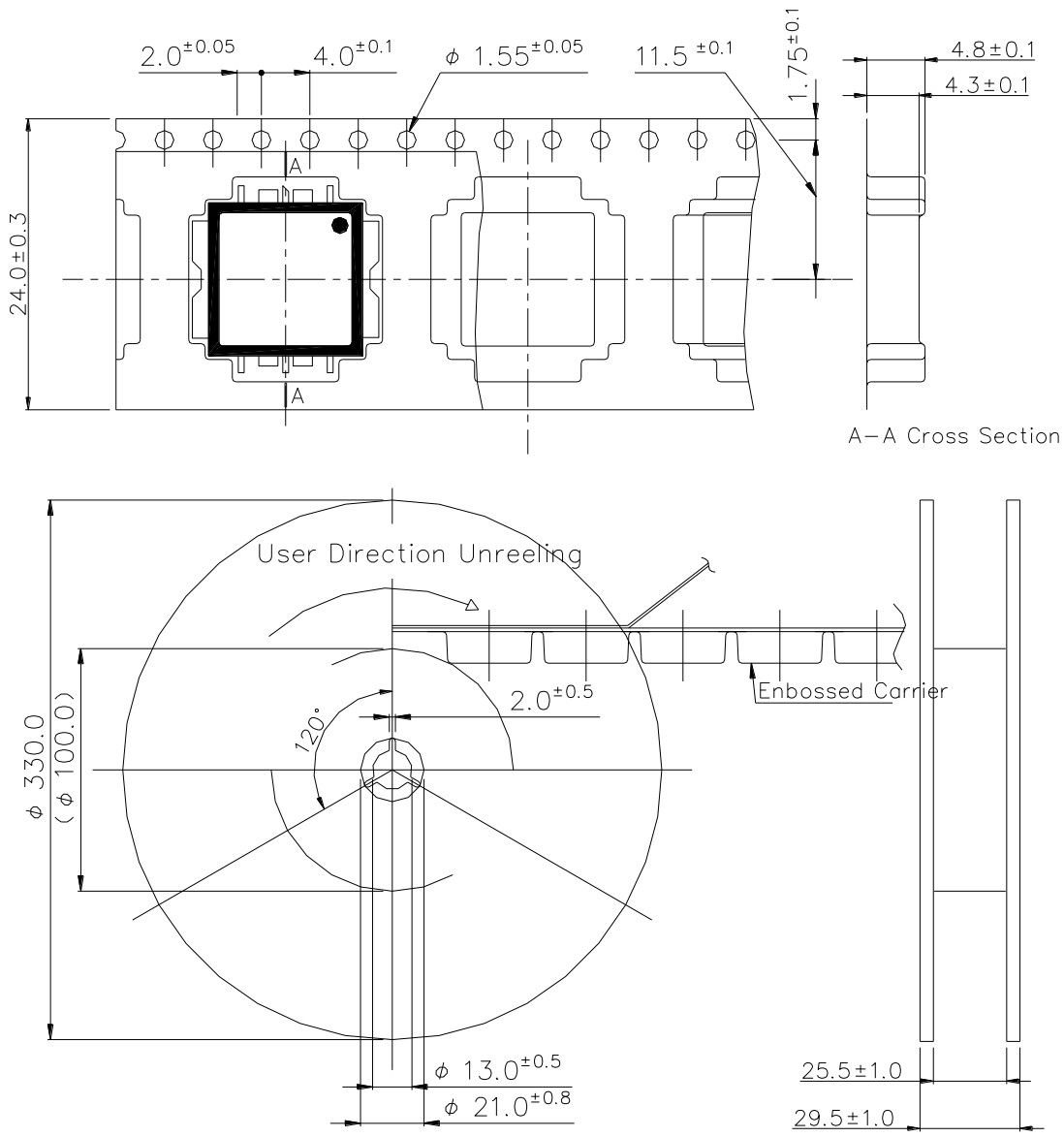
Code	H	M	N	P	R	S	T	U	W	X	Y	Z
Month	1	2	3	4	5	6	7	8	9	10	11	12

PKG	Marking	Type Nunber	Part Number
I2C	(ex.I2C PKG)  	ELM****-*** SGK****-*** ex. ELM5964-7PS SGK5872-20A	****-*** ****-*** 5964-7PS 5872-20A

- **JEDEC Tray Dimension (Part No:ELM5964-7PS)**



- **Tape/Reel Configuration**
  (Part No:ELM5964-7PST)



Quantity: 500pcs/tape
Tape Material: Conductive PS

(unit in mm)

● Mounting Instructions for Package for Lead-free solder

Mounting Condition

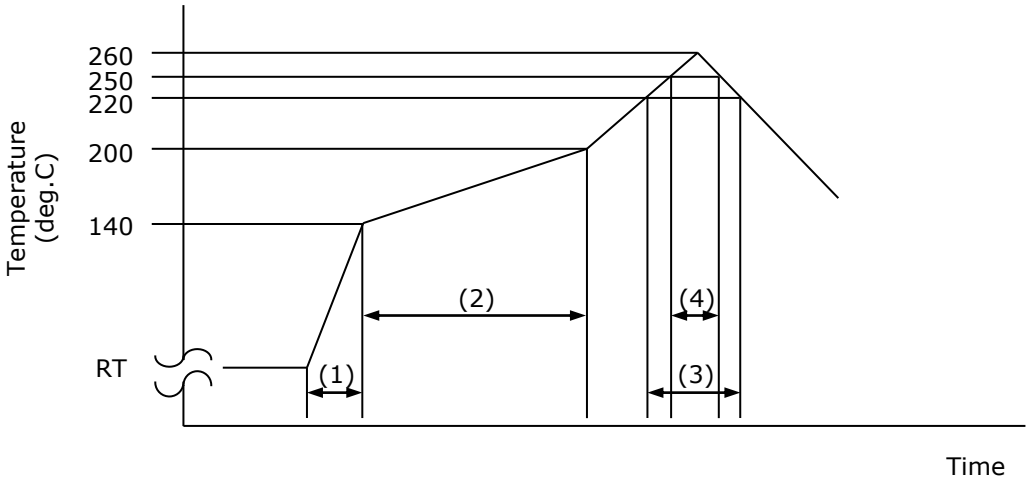
For soldering, Lead-free solder (Sn-3.0Ag-0.5Cu)*1 or equivalent shall be used.

1. The example solder is a tin-rich alloy with 3.0% silver and 0.5% copper, often called Sn 96 for its approximate Tin content.
2. A rosin type flux with chlorine content of 0.2% or less shall be used. The rosin flux with low halogen content is recommended. When soldering, use the following time/ temperature profile with any of the methods listed for acceptable solder joints.
3. Make sure the devices have been properly prepared with flux prior soldering.

*** Reflow soldering method (Infrared reflow / Heat circulation reflow / Hot plate reflow);**

Limit solder to 3 reflow cycles because resin is used in the modules manufacturing process. Excessive reflow will effect the resin resulting in a potential failure or latent defect.
The recommended reflow temperature profile is shown below. The temperature of the reflow profile must be measured at the device lead.

● Reflow temperature profile and condition:



(1). Temperature rise: 3 deg.C/seconds.
(2). Preheating: 150 to 200 deg.C, 60 to 180seconds.
(3). Main heating: 220 deg.C, 60 seconds max.
(4). Main heating: 260 deg.C max., more than 250 deg.C, 20 to 40 seconds max.
* Measurement point: Device Heat-sink (Source Pin).

1. The above-recommended conditions were confirmed using the manufacturer's equipment and materials. However, when soldering these products, the soldering condition should be verified by customer using their own particular equipment and materials.

● Cleaning

Avoid washing of the device after soldering by reflow method due to the risk of liquid absorption by the resin used in this part.

Humidity Lifetime for ELMxxxx-7PST

The following graph shows the effect of moisture on lifetime (moisture resistance) for the **ELMxxxx-7PST**. Each graph indicates the MTTF and failure rate prediction (Confidential Level = 90 %) which calculated from the results of highly accelerated temperature and humidity stress test (HAST).

Representative of device type : ELM7179-7PST

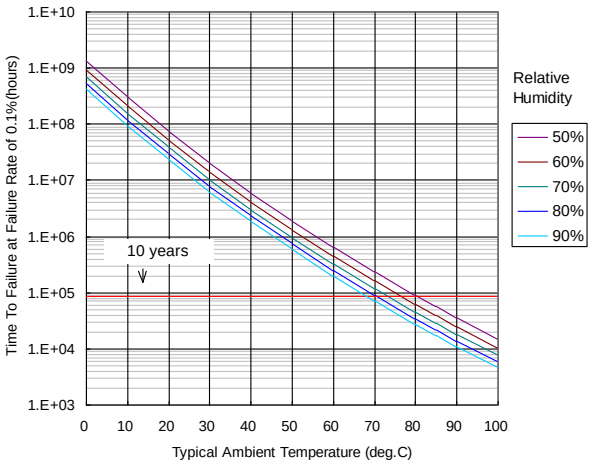
Subject of device type : ELMxxxx-7PST

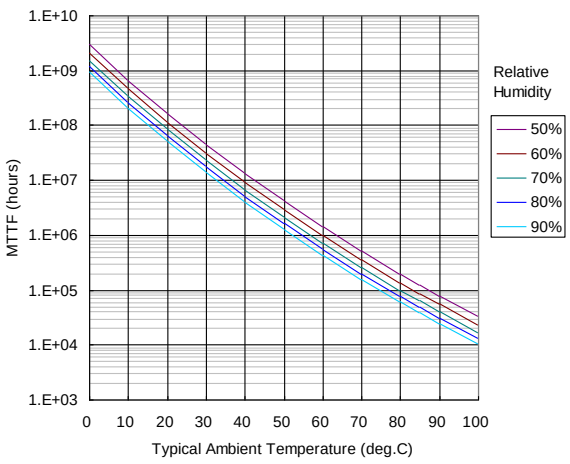
Field environmental conditions for operation

If the **ELMxxxx-7PST** is installed in a non-hermetic environment, please refer to the following recommendations and notes for design with, and assembly and use of our products.

Note 1. When drain current cuts off, it should be cut off by drain bias, and not cut off by gate bias only. The humidity lifetime becomes shorter in case of the gate-only cut off operation due to electric field strength interacting with humidity.

Note 2. **ELMxxxx-7PST** should be used under the environment conditions of no dew condensation. These plots do not apply in the case of liquid absorbed into the resin, whether applied to the part in assembly or as condensate in the application.





For Safety, Observe the Following Procedures Environmental Management

- Do not put this product into the mouth.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Respect all applicable laws of the country when discarding this product.
  This product must be disposed in accordance with methods specified by applicable hazardous waste procedures.

Any information, such as descriptions of a function and examples of application circuits, in this document are presented solely as a reference for the purpose to show examples of operations and uses of Sumitomo Electric semiconductor device(s); Sumitomo Electric does not warrant the proper operation of the device(s) with respect to its use based on such information. When the user develops equipment incorporating the device(s) based on such information, they must assume full responsibility arising out of using such information. Sumitomo Electric assumes no liability for any damages whatsoever arising out of the use of the information.

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Any semiconductor devices have an inherent chance of failure. You must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of excessive current levels and other abnormal operating conditions.

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