

#### C-Band Internally Matched FET

#### **FEATURES**

High Output Power: P1dB=36.0dBm (Typ.)

High Gain: G1dB=10.0dB (Typ.) High PAE: ηadd=34% (Typ.) Broad Band: 7.7 to 8.5GHz

Internally matched

Plastic Package for SMT applications

#### **DESCRIPTION**

The ELM7785-4PS is a power GaAs FET that is internally matched for standard communication bands to provide optimum power and gain.



ABSOLUTE MAXIMUM RATING (Case Temperature Tc=25 deg.C)

Item	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	15	V
Gate-Source Voltage	V <sub>GS</sub>	-5	V
Total Power Dissipation	P <sub>T</sub>	27.3	W
Storage Temperature	T <sub>STG</sub>	-40 to +125	deg.C
Channel Temperature	T <sub>CH</sub>	175	deg.C

RECOMMENDED OPERATING CONDITION (Case Temperature Tc=25 deg.C)

Item	Symbol	Condition	Limit	Unit
DC Input Voltage	$V_{DS}$		<10	V
Forward Gate Current	I <sub>GF</sub>	R <sub>G</sub> =100 ohm	<+16	mA
Reverse Gate Current	I <sub>GR</sub>	R <sub>G</sub> =100 ohm	>-2.2	mA
Channel Temperature	T <sub>CH</sub>		155	deg.C

**ELECTRICAL CHARACTERISTICS (Case Temperature Tc=25 deg.C)** 

Item	Symbol	Condition	Limit			Unit	
nem	Symbol	Condition	Min.	Тур.	Max.	Ullit	
Drain Current	I <sub>DSS</sub>	$V_{DS}=5V$ , $V_{GS}=0V$	-	1700	2600	mA	
Trans conductance	gm	V <sub>DS</sub> =5V, I <sub>DS</sub> =1100mA	-	1700	-	mS	
Pinch-off Voltage	V <sub>P</sub>	V <sub>DS</sub> =5V, I <sub>DS</sub> =85mA	-0.5	-1.5	-3.0	V	
Gate-Source Breakdown Voltage	$V_{GSO}$	I <sub>GS</sub> =85uA	-5.0	-	-	V	
Output Power at 1dB G.C.P.	P <sub>1dB</sub>		35.0	36.0	-	dBm	
Power Gain at 1dB G.C.P.	G <sub>1dB</sub>	V <sub>DS</sub> =10V	8.5	10.0	-	dB	
Drain Current	I <sub>dsr</sub>	$I_{DS(DC)}=0.65I_{DSS}(typ.)$	-	1100	1300	mA	
Power Added Efficiency	$\eta_{add}$	f=7.7 to 8.5 GHz	-	34	-	%	
Gain Flatness	ΔG		-	-	1.2	dB	
3 <sup>rd</sup> Order Inter Modulation Distortion	IM <sub>3</sub>	f=8.5GHz Δf=10MHz, 2-tone Test Pout=25.5dBm (S.C.L.)	-40	-43	-	dBc	
R <sub>th</sub>	R <sub>th</sub>	Channel to Case	-	4.5	5.5	deg.C/W	
$\Delta T_{ch}$	$\Delta T_{ch}$	10V x I <sub>dsr</sub> x R <sub>th</sub>	-	-	71.5	deg.C	



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#### **CASE STYLE: 12C**

ESD	Class 3 A	4000 to 8000V
MSL	2A	4 weeks after open the package
PAHS COMPLIANCE	Voc	

**Ordering Information** 

Model Type	MOQ	MOU	Packing Style
ELM7785-4PS	15pcs	15pcs	15pcs Tray
ELM7785-4PST	500pcs	500pcs	24mm width Tape (500pcs/Reel)

<sup>\*</sup>MOQ stands for Minimum Order Quantity.

#### Note

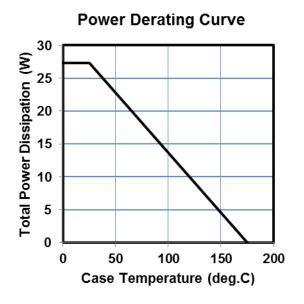
- This device will not be delivered with test data but tested pass/fail 100% against DC and RF specifications.
- NO liquid cleaning process is suitable for this device. (including de-ionized water or solvent)

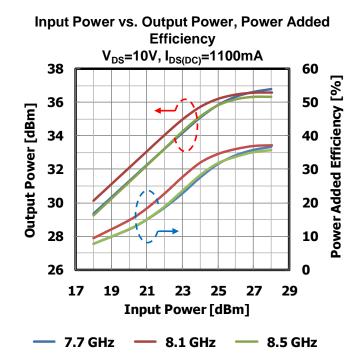
<sup>\*</sup>MOU stands for Minimum Order Unit size.

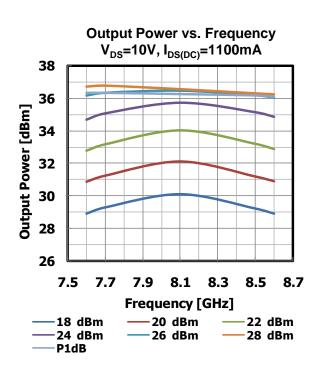


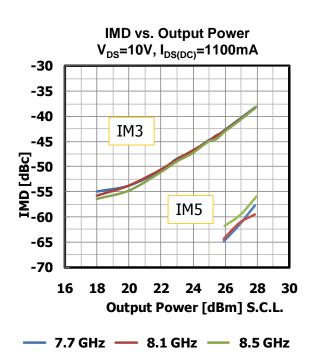
#### C-Band Internally Matched FET

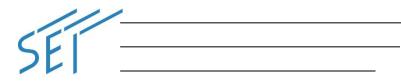
#### RF Characteristics



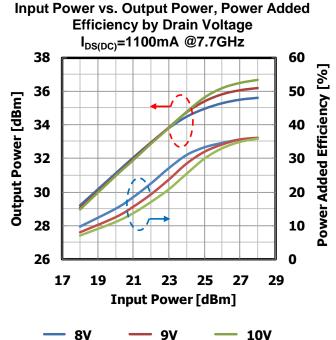


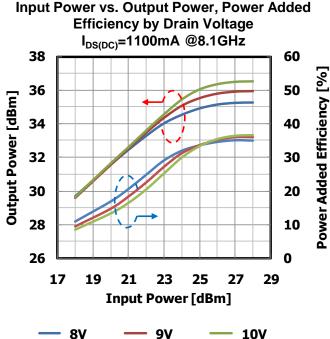




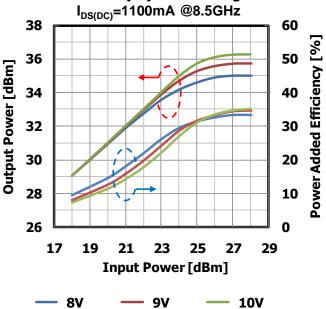


#### C-Band Internally Matched FET





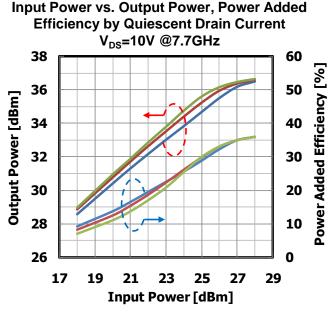
# Input Power vs. Output Power, Power Added Efficiency by Drain Voltage I<sub>DS(DC)</sub>=1100mA @8.5GHz

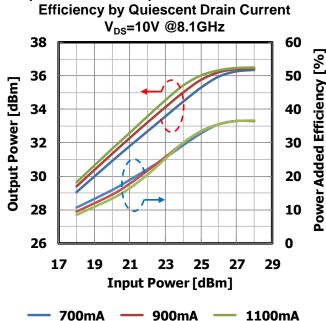




1100mA

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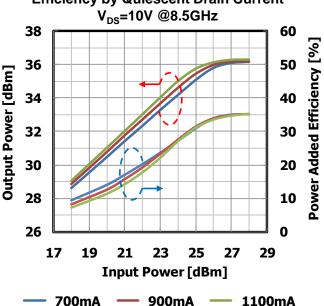
Input Power vs. Output Power, Power Added

Input Power vs. Output Power, Power Added **Efficiency by Quiescent Drain Current** 

900mA

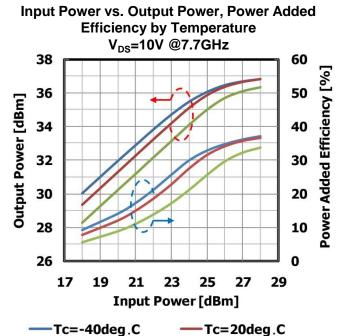
1100mA

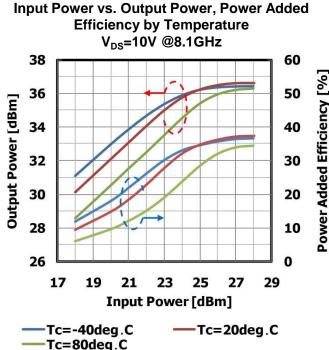
700mA





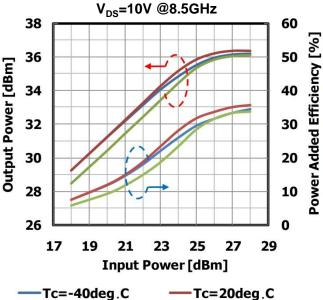
#### C-Band Internally Matched FET





Input Power vs. Output Power, Power Added Efficiency by Temperature

Tc=80deg.C

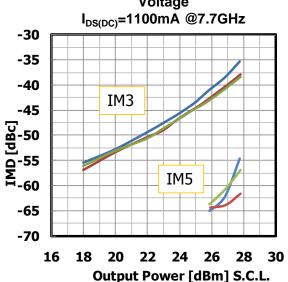


Tc=80deg.C



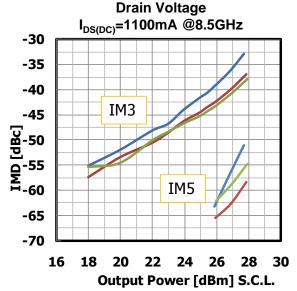
#### C-Band Internally Matched FET

# IMD Performance vs. Output Power by Drain Voltage

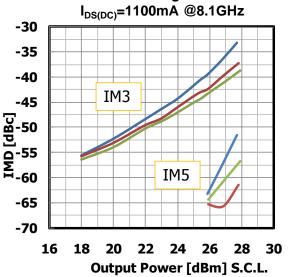


$$--$$
Vd[V]= 8  $--$ Vd[V]= 9  $--$ Vd[V]= 10

# IMD Performance vs. Output Power by



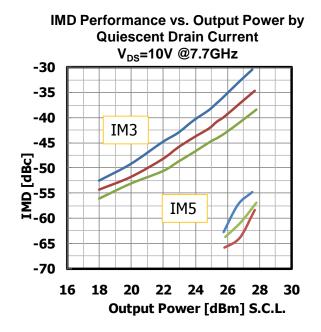
# IMD Performance vs. Output Power by Drain Voltage

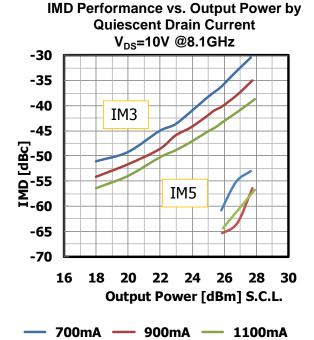


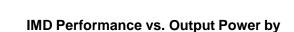
$$--Vd[V] = 8 ---Vd[V] = 9 ---Vd[V] = 10$$



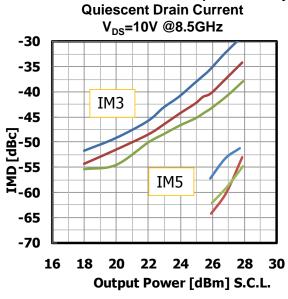
#### C-Band Internally Matched FET







700mA — 900mA — 1100mA



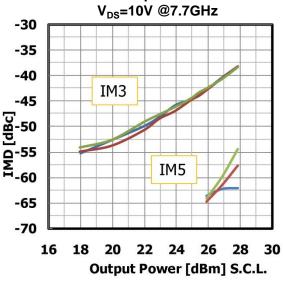
700mA — 900mA — 1100mA



#### C-Band Internally Matched FET

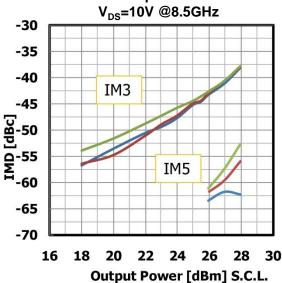
-Tc=20deg.C

# IMD Performance vs. Output Power by **Temperature** V<sub>DS</sub>=10V @7.7GHz





#### **IMD Performance vs. Output Power by Temperature**





#### IMD Performance vs. Output Power by Temperature V<sub>DS</sub>=10V @8.1GHz -30 -35 -40 IM3 -45 OWI -55 -60 IM5 -65 -70 22 26 16 18 20 24 28 30 Output Power [dBm] S.C.L.

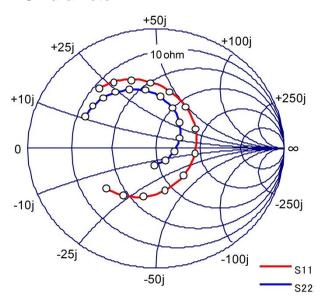
Tc=-40deg.C

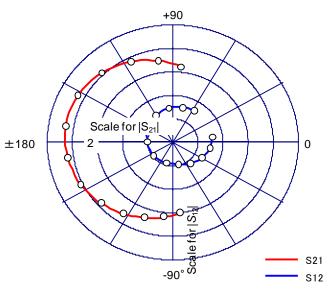
Tc=80deg.C



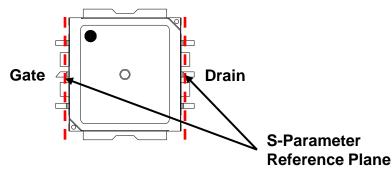
#### C-Band Internally Matched FET

#### S-Parameter

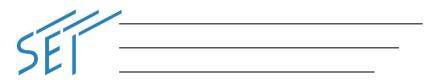




Frequency	S	11	S	21	S	12	S	22
(MHz)	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
7500	0.672	131.1	3.002	-85.0	0.032	7.7	0.617	154.1
7600	0.645	120.5	3.184	-96.8	0.030	-9.0	0.610	144.4
7700	0.607	108.5	3.395	-109.5	0.027	-29.2	0.592	134.3
7800	0.554	94.3	3.610	-123.0	0.023	-51.4	0.570	123.8
7900	0.489	77.5	3.840	-137.7	0.020	-76.0	0.536	112.6
8000	0.420	56.6	4.084	-153.7	0.018	-105.2	0.495	99.5
8100	0.350	27.8	4.236	-171.0	0.019	-143.1	0.434	84.9
8200	0.307	-7.8	4.317	170.9	0.020	179.1	0.364	68.7
8300	0.315	-46.3	4.272	152.2	0.023	146.3	0.286	49.1
8400	0.361	-78.9	4.086	133.9	0.026	119.3	0.207	24.5
8500	0.419	-103.6	3.806	116.0	0.029	95.3	0.148	-9.6
8600	0.471	-122.6	3.507	99.6	0.031	74.8	0.127	-54.1
8700	0.514	-138.6	3.204	84.3	0.032	57.3	0.146	-93.4

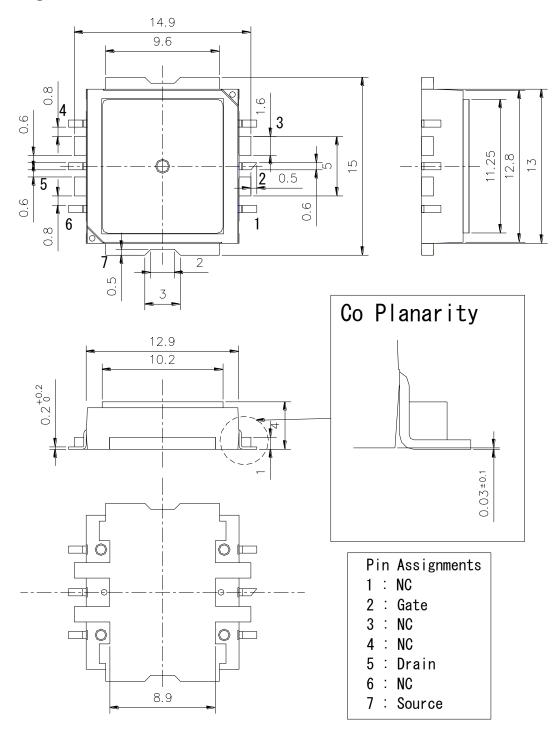


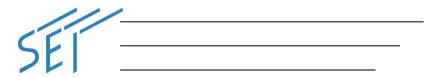
SUMITOMO ELECTRIC



#### C-Band Internally Matched FET

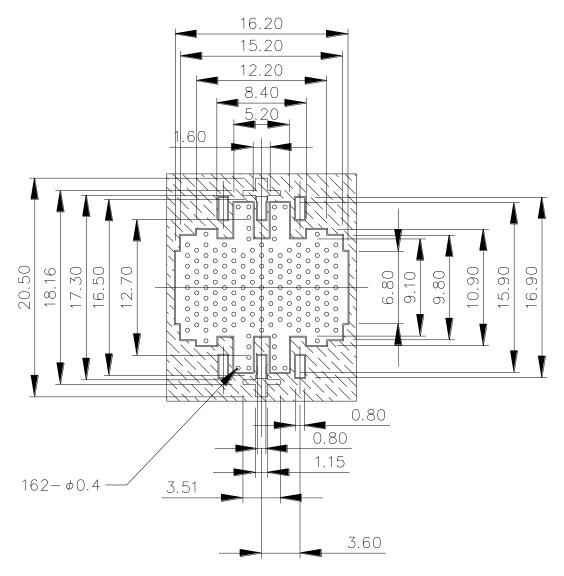
#### Package Outline





#### C-Band Internally Matched FET

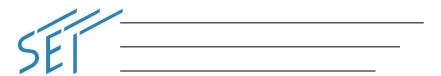
#### PCB Pads and Solder-Resist Pattern



#### Notes:

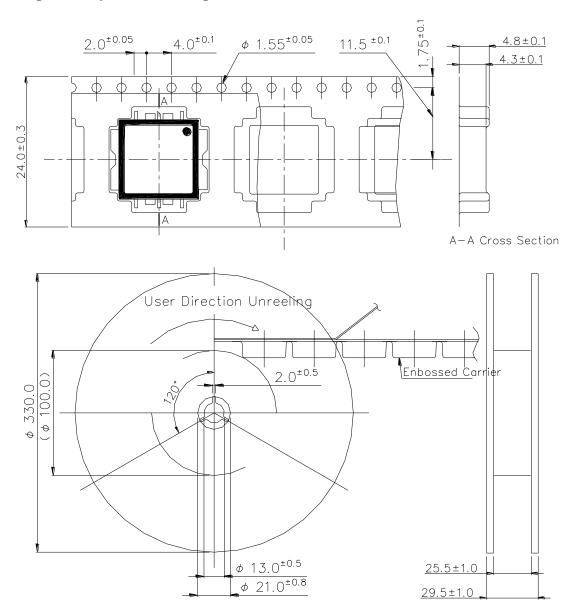
1. Laminate: Rogers Corporation RO4003, Thickness t=0.508mm, Cu Foil 18um. Finish to copper foil: Ni 0.1um min. / Au 0.1um (Both side).

2. Resist



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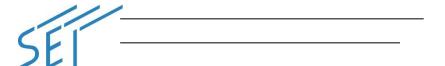
#### Marking and Tape/Reel Configuration



Quantity: 500pcs/tape Tape Material: Conductive PS

(unit in mm)





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#### Mounting Instructions for Package for Lead-free solder

#### **Mounting Condition**

For soldering, Lead-free solder (Sn-3.0Ag-0.5Cu)\*1 or equivalent shall be used.

- 1. The example solder is a tin-rich alloy with 3.0% silver and 0.5% copper, often called Sn 96 for its approximate Tin content.
- 2. A rosin type flux with chlorine content of 0.2% or less shall be used. The rosin flux with low halogen content is recommended. When soldering, use the following time/ temperature profile with any of the methods listed for acceptable solder joints.
- 3. Make sure the devices have been properly prepared with flux prior soldering.

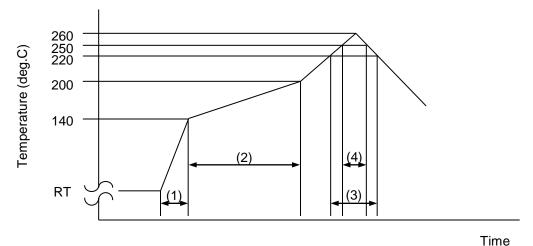
#### \* Reflow soldering method (Infrared reflow / Heat circulation reflow / Hot plate reflow);

Limit solder to 3 reflow cycles because resin is used in the modules manufacturing process.

Excessive reflow will effect the resin resulting in a potential failure or latent defect.

The recommended reflow temperature profile is shown below. The temperature of the reflow profile must be measured at the device lead.

#### Reflow temperature profile and condition:



(1). Temperature rise: 3 deg.C/seconds.

(2). Preheating: 150 to 200 deg.C, 60 to 180seconds.

(3). Main heating: 220 deg.C, 60 seconds max.

(4). Main heating: 260 deg.C max., more than 250 deg.C, 20 to 40 seconds max.

\* Measurement point: Device Heat-sink (Source Pin).

1. The above-recommended conditions were confirmed using the manufacturer's equipment and materials. However, when soldering these products, the soldering condition should be verified by customer using their own particular equipment and materials.

#### Cleaning

Avoid washing of the device after soldering by reflow method due to the risk of liquid absorption by the resin used in this part.



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#### **Humidity Lifetime for ELMxxxx-4PST**

The following graph shows the effect of moisture on lifetime (moisture resistance) for the ELMxxxx-4PST. Each graph indicates the MTTF and failure rate prediction (Confidential Level = 90 %) which calculated from the results of highly accelerated temperature and humidity stress test (HAST).

Representative of device type : ELM7179-4PST

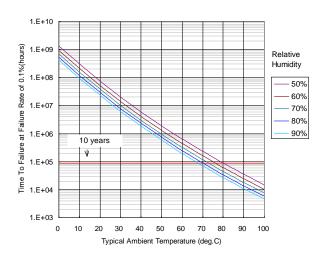
Subject of device type : ELMxxxx-4PST

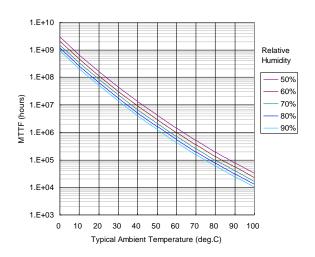
#### Field environmental conditions for operation

If the **ELMxxxx-4PST** is installed in a non-hermetic environment, please refer to the following recommendations and notes for design with, and assembly and use of our products.

Note 1. When drain current cuts off, it should be cut off by drain bias, and not cut off by gate bias only. The humidity lifetime becomes shorter in case of the gate-only cut off operation due to electric field strength interacting with humidity.

Note 2. **ELMxxx-4PST** should be used under the environment conditions of no dew condensation. These plots do not apply in the case of liquid absorbed into the resin, whether applied to the part in assembly or as condensate in the application.







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#### For further information please contact:

http://global-sei.com/Electro-optic/about/office.html

#### CAUTION

This product contains **gallium arsenide (GaAs)** which can be hazardous to the human body and the environment. For safety, observe the following procedures:

- •Do not put these products into the mouth.
- •Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- •Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.